Bansilal Ramnath Agarwal Charitable Trust’s

Vishwakarma Institute of Technology

(An Autonomous Institute affiliated to University of Pune)

Structure & Syllabus of

B.E. (Information Technology)

Pattern ‘E11’

Effective from Academic Year 2013-14

Prepared by: - Board of Studies in Information Technology

Approved by: - Academic Board, Vishwakarma Institute of Technology, Pune

Signed by,

Chairman – BOS          Chairman – Academic Board

Structure & Syllabus of  B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
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$ Please Refer Page No. 252 (Academic Information Section)
!
Please Refer F.Y. B.E. Syllabi Booklet
@
Please Refer GP-PD-OE Structure & Syllabi Booklet
Program Educational Objectives (PEO)
B.E. (Information Technology)

1. Program and Course Objectives
Programme Objectives:

<table>
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<th>PEOs :-</th>
<th>Description of the Objective</th>
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<td>I</td>
<td><strong>Preparation:</strong> To prepare the students with a commitment towards intellectual, creative and professional growth by application of innovative practices widely accepted by industry or global educational platform.</td>
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<td>II</td>
<td><strong>Core competence:</strong> To provide students with foundation in application of mathematical &amp; engineering fundamentals to computing solutions that can result in product or process.</td>
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<tr>
<td>III</td>
<td><strong>Breadth:</strong> To enable student to exercise problem solving capacity with effective use of analysis, design, development that address idea realization.</td>
</tr>
<tr>
<td>IV</td>
<td><strong>Professionalism:</strong> To inculcate students with professional and ethical values communication and collaboration skill and involvement in team work as a member having multidisciplinary knowledge useful to the society.</td>
</tr>
<tr>
<td>V</td>
<td><strong>Learning Environment:</strong> To provide students an academic environment that developed leadership qualities, excellent in subject area of computer engineering and lifelong learning in every sphere of their life.</td>
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</table>

Course Objectives: Course objectives are specified in the course syllabus.
2. Program and Course Outcomes
Programme Outcomes:

a. **Broad foundations:** Graduates will understand and apply mathematical foundations, algorithmic principles, and computer science theory in the modeling and design of computer-based systems in a way that demonstrates comprehension of the tradeoffs involved in design choices.

b. **Disciplinary Foundation:** Graduates will recognize the need for, and an ability to engage in, continuing professional development and demonstrate an ability to use current techniques, skills, and tools necessary for computing practices.

c. **Specialization:** Graduates will have understanding of and ability to apply the concepts and skills related to writing technical documents such as specifications, design and users manuals in appropriate formats.

d. **Design:** Graduates will be able to analyze, design, implement, and test a solution to real world problems including appreciating the value of efficient design created to meet clearly developed requirements.

e. **Innovations:** Graduates will demonstrate ability to formulate and answer empirical questions through participation in projects especially addressing design and deployment, of computing infrastructure with technology integration and user-centered design

f. **Communication skills:** Graduates will demonstrate ability to communicate effectively through verbal and written form.

g. **Interpersonal skills:** Graduates should be able to interact professionally with others in the workplace and engage themselves, effectively in team work for group projects.

h. **Engineering and society:** Graduates will understand professional ethical and social responsibility which will prepare then to address local and global impact of engineering solutions.

i. **Engineering Applications:** Graduates will understand and apply engineering artifacts of engineering solutions in meaningful and useful way to society and global environment.

j. **Lifelong Learning:** Graduate will acquire skills necessary to engage in life long learning and understanding of need to continuity improve the skills in refining and updating the knowledge base.

Course Outcomes: Course outcomes are specified in the course syllabus
MODULE III
### Structure, S.E. (Module III)

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<th>Subject Name</th>
<th>Teaching Scheme (Hrs/week)</th>
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<td>CS20101</td>
<td>Data structures</td>
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<td>S₂</td>
<td>CS20103</td>
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**Total** 14 2 10 22
CS26101 :: APPLIED ELECTRONICS

Credits: 02  
Teaching Scheme: - Theory 2 Hrs/Week

Prerequisites: Nil

Objectives:
- To study the basic components like diode, BJT, Op-Amps and their applications.
- To Study the basic working of active components and their implementation in electronic circuits.

Unit I  
Diodes (5+1 Hrs)

B. Full wave rectifier, Bridge rectifier and analyze these rectifiers w. r. t ripple factor, efficiency.

Unit II  
Bipolar Junction Transistor (5+1 Hrs)

A. Introduction to BJT, BJT configuration, Characteristics of CE, Overview of Biasing, Fixed Bias, Collector to Base Bias, Self Bias, Stability factor for self bias, Applications of transistors:-Transistor as a switch and amplifier
B. Thermal Runaway, Bias Compensation.

Unit III  
Operational Amplifiers (5+1 Hrs)

B. Typical parameter values of Op-Amp (IC-µA741)
Unit IV  

**OpAmp Applications**

A. Adder, Precision Rectifier, Zero Crossing Detector, Schmitt Trigger, Waveform Generator (triangular), Integrator, Instrumentation Amplifier, Current to Voltage Circuits.

B. Subtractor, Differentiator, and Voltage to Current Circuits.

Unit V  

**Power Devices and Power Supplies**

A. Power Devices: - Characteristics and Principle of Operation of SCR, Applications: Half-wave and Full-wave Controlled rectifiers, Fan Regulator, UJT as relaxation oscillator. Power supplies: - Introduction to SMPS, types of SMPS: Buck, Boost, Buck-Boost SMPS, online UPS, off line UPS, Line interactive UPS

B. SCR Applications: Half-wave wave Controlled rectifiers, Introduction to DIAC and TRIAC.

**Text Books**


**Reference Books**


**Additional Reading**


CS20101:: DATA STRUCTURES

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Computer Programming.

Objectives:
- To introduce, fundamental data structures and problem solving paradigms
- To introduce time complexity analysis of problems.
- To study the representation, implementation & applications of data structures.
- To compare alternative implementations of data structures.
- To choose the appropriate data structure for modeling a given problem.
- Mapping with PEOs :- I, (a).

Unit I  
(9+2 Hrs)
Stack and Queue using Linear Data Structures

A. Polynomial representation using arrays, operations on polynomials like add, multiply, evaluate, Representation of sparse matrix, simple and fast transpose.


B. Algorithm for sparse matrix addition, Expression Conversions and evaluation with respect to stack

Unit II  
(8+2 Hrs)
Linked Representation

Equality.

B. Stack using Linked List, Queue using Linked List, Finding Depth of GLL

Unit III

(9+2 Hrs)

Tree

A. Binary trees and its representation using sequential and linked organization, full and complete binary trees, Creation of a binary tree, binary tree traversals (recursive and non recursive), operations such as copy, equal etc. Binary search tree, creation of binary Search tree, finding height and counting leaf nodes of a binary search tree (with and without recursion), Finding mirror image of the binary search tree with and without recursion, Deletion of a node from a binary search tree. Printing a tree level wise and depth wise, Threaded binary trees, Creation and traversal of in-order, pre-order and post-order threaded binary tree, Insertion and deletion of nodes in threaded binary tree, AVL Trees. Applications of binary trees: Gaming, Expression tree, Heap sorting; OBST and B. B- tree, B+tree, red-black trees.

Unit IV

(7+1 Hrs)

Graph

A. Review of basic terminology, Representation of graphs using adjacency matrix, adjacency list, Traversals: Depth First and Breadth First, Connected components and spanning trees, Kruskal’s and Prim’s algorithms for minimum spanning tree, Algorithm for shortest path- Dijkstra’s algorithm, Warshal’s Algorithm.

B. Graph applications : Multistage Graph Problem

Unit V

(7+1 Hrs)

Hashing and Files

A. Hash tables, Hash functions: Division, folding and Mid square methods, Collusion Resolution Strategies: Linear Probing, rehashing, Open addressing and Chaining, Chaining with and without replacement, Table Overflow, Extendible hashing.

Files: Definition and concepts, File organizations, File Operations, Processing of sequential, Index-sequential and direct files.

B. Open Addressing hashing, Quadratic Probing, Chaining with linked list.

Text Books


Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’, Issue 03, Rev01 dated 02/04/2011

Reference Books

Additional Reading
CS20103:: APPLIED PROBABILITY AND STATISTICS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Elementary knowledge of 1) Numbers 2) Calculus 3)Basics of Algorithms

Objectives:
- To understand basic concepts of probability distributions and statistics.
- To learn how to design source compression codes to improve the efficiency of information transmission.
- To understand queuing theory and application of queuing models.
- Mapping with PEOs: I, (a).

Unit I  (8+1 Hrs)
Probability

A. Concept of probability, sample spaces, mutually exclusive and independent events, theorems on probability, conditional probability, Baye’s Theorem, Discrete and continuous random variables, probability density functions, cumulative distribution functions, expectation, variance, standard deviation, moments, covariance and correlation, Binomial, hypergeometric, Poisson, normal probability distributions and relation between them.
B. Law of large numbers, central limit theorem, Chebyshev’s inequality, moment generating functions, exponential and gamma distribution.

Unit II  (8+1 Hrs)
Statistics

A. Sampling Theory, Population and sample, sampling distribution of Sample mean (σ known and unknown), variance, Chi-square distribution, t-distribution and F-distribution, biased and unbiased estimation, point and interval estimates, statistical hypothesis, tests of hypothesis, one tailed and two tailed tests, type I and type II errors, level of significance, P value approach.
B. Chi square test for goodness of fit, power of a test, curve fitting and regression.

Unit III  (8+1 Hrs)
Reliability Theory
A. Definition of reliability, Failure models – constant hazard, linear hazard, non linear hazard, fault tree analysis, event tree analysis, reliability block diagrams, system reliability, redundancy, component importance.
B. Concept of maintainability and availability, life data analysis.

Unit IV (8+1 Hrs)
Information theory

A. Information theory – Self Information, FLC & VLC, Huffman Coding, Entropy and Uncertainty information content, Rate of a language, redundancy. Number theory – Division Algorithm, Euclid’s Algorithm, Fundamental Theorem of Arithmetic, Congruence’s, Fermat’s little theorem, Chinese remainder theorem.
B. Introduction to data Compression Application, speech Compression, Telemetry Compression.

Unit V (8+1 Hrs)
Queuing Theory

A. Introduction to Queuing System, Queue characteristics, Birth-Death process, Queuing Models-Poisson Arrival Model, Little’s Theorem, M/M/1 Queues - M/M/1:∞, M/M/s:∞ models.
B. M/M/1:K and other Markov systems

Text Books

Reference Books
5. ‘Elementary Number Theory’, David Burton

Additional readings
CS20105 :: PRINCIPLES OF PROGRAMMING LANGUAGES

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: nil

Objectives:
- To learn the fundamental concepts of programming languages & the various programming paradigms.
- To increase the capacity to express ideas using a programming language.
- To learn to choose an appropriate programming language for a designed task.
- To learn the important features of different types of languages and their relevance.
- To learn past the superficial differences in languages and recognize the commonalities in meaning.
- To develop the ability to quickly learn new programming languages.
- Mapping with PEOs: II, III, (d).

Unit I  
Introduction to Programming languages  
(8+1 Hrs)


B. Comparison of different programming paradigms and representative languages of each.

Unit II  
Imperative & Procedural Programming  
(8+2 Hrs)


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B. Generic Templates in C++, Library Classes in C++.

Unit III (8+2 Hrs)
Object – Oriented Programming (Java-I)

Similarities and differences between C++ and Java: destructors, access modifiers, inheritance, polymorphism, garbage collection.
B. Exception handling in C++, multiple inheritance in C++.

Unit IV (8+1 Hrs)
Object – Oriented Programming (Java-II)


B. Concurrency Issues with multi-threading, deadlock.

Unit V (8+2 Hrs)
Case Studies of different languages

A. Web programming (Overview of HTML, CSS, JavaScript, PHP), functional programming (Overview of Haskell), concurrent programming (Overview of Erlang), logic programming (Overview of PROLOG), Overview of Matlab. Visual programming, Multi-paradigm languages.

B. Overview of LISP (CommonLISP and Scheme)
Text Books

Reference Books

Additional Reading
CS20107 :: DATA COMMUNICATION

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To understand the basics of signals, and modulation techniques.
- To understand multiplexing techniques.
- To understand the various transmission techniques.
- To understand basic concepts of computer networks.
- Mapping with PEOs :- I, II, (a),(b).

Unit I
Introduction to Electronic Communication

A. The importance of Communication, Elements of communication system, Types of electronics communication, Electromagnetic spectrum, Bandwidth, Signal Types, Noise: internal, External, Noise calculation, Shannon-Hartley theorem.
Introduction to Fourier series, Fourier Transform- DFT, IFT.
B. Survey of communication applications. Numerical based on Fourier transform properties.

Unit II
Modulation Techniques

A. Principles of Amplitude Modulation, Modulation index and percentage of modulation, AM power distribution, Single sideband communication, AM transmitters and Receivers. Frequency modulation principles, sideband and modulation index, Phase modulation.
Receivers: Superheterodyne receiver, Frequency conversion, Intermediate frequency selection and Images.
B. FM vs. AM, FM vs. PM, AM vs. PM, Numerical based on AM, FM.

Unit III
Multiplexing and Communication

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011

B. Frequency hopping spread spectrum, Direct sequence spread spectrum. Cellular Telephone System

Unit IV
Transmission and Propagation


B. Transmission media wired and wireless

Unit V
Communication Networks

A. Types of Networks: LAN, WAN, MAN. Network Topology, Network Software: Protocol hierarchy, Design Issues for layers, Connection oriented and connectionless services, OSI and TCP/IP reference model, IP addresses

B. Comparison of OSI and TCP/IP reference model, overview of connecting devices: Repeater, Hub, Bridge, Router.

Text Books

Reference Books

Additional Reading
CS20201:: DATA STRUCTURES

Credits: 01

Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: Computer Programming

Objectives:
- To introduce, fundamental data structures and problem solving paradigms
- To introduce time complexity analysis of problems.
- To study the representation, implementation & applications of data structures.
- To compare alternative implementations of data structures.
- To choose the appropriate data structure for modeling a given problem.
- Mapping with PEOs :- I, (a).

List of Contents

A TERM-WORK containing the record of the following:

1. Algorithm to implement single variable polynomial using array and perform addition, multiplication of them.

2. Algorithm to implement conversion of infix expression to prefix and evaluate it using stack.

3. Algorithm to implement Double Ended Queue using array.

4. Algorithm to implement doubly linked list.

5. Algorithm to implement creation of a binary tree and traverse it in preorder, postorder and inorder way, both by recursion and non-recursion.

6. Algorithm to implement creation of a binary Search tree and find its mirror image. Print original and mirror image level wise.

7. Algorithm to represent a graph using adjacency list and perform DFS and BFS.

8. Algorithm to implement Heap Sort.

9. Algorithm to implement creation of a hash table and handle the collisions using
linear probing with or without replacement.

10. Algorithm to implementation of sequential file and perform insertion of record, display, delete and modify, search operations on it.

**Text Books**


**Reference Books**


**Additional Reading**

CS20203 :: APPLIED PROBABILITY AND STATISTICS

Credits: 01

Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: Elementary knowledge of 1) Numbers 2) Calculus 3) Basics of Algorithms and syllabus covered unit-wise in theory lectures.

Objectives:
Upon completion of this module students will be able to:
- Recognize mathematical structures in practical problems.
- Translate problems into mathematical language and analyze problems using methods from all the units.
- Mapping with PEOs: I, (a).

List of Contents

In this module students will work on problems to practice and apply methods introduced in the lectures. Discussions of problems in small groups is always encouraged and facilitated and students are asked to submit weekly home work assignments and provide them immediate feedback and support materials.

Tutorial No. 1: Summary on probability and conditional probability, Baye’s Theorem, random variables, density functions and cumulative distribution functions.

Tutorial No. 2: Probability distributions, Binomial, hypergeometric, Poisson, normal probability distributions and problems solving.

Tutorial No. 3: Sampling Theory, Population and sample, Chi-square, t-distribution and F-distribution.

Tutorial No. 4: Tests of hypothesis, one tailed and two tailed tests, type I and type II errors, level of significance, P value approach, goodness of fit.

Tutorial No. 5: Hazard models, reliability block diagrams.

Tutorial No. 6: System reliability, redundancy, fault tree analysis.

Tutorial No. 7: FLC & VLC, Problems on Self Information, Huffman Coding, Entropy.
Tutorial No. 8: Number theory - Modula arithmetic, Fermat’s little theorem, Chinese remainder theorem

Tutorial No. 9: Birth-Death process, Poisson Arrival Model, Little’s Theorem, M/M/1 Queues - , M/M/1:∞ model

Tutorial No. 10: M/M/s:∞ model, M/M/1:K model

Tutorial No. 11: Law of large numbers, central limit theorem, Chebyshev’s inequality, moment generating functions

Tutorial No. 12: Curve fitting and Regression

Text Books

Reference Books
CS20301:: DATA STRUCTURES

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Computer Programming

Objectives:
- To introduce, fundamental data structures and problem solving paradigms
- To introduce time complexity analysis of problems.
- To study the representation, implementation & applications of data structures.
- To compare alternative implementations of data structures.
- To choose the appropriate data structure for modeling a given problem.
- Mapping with PEOs :- I, (a).

List of Practical

1. Write a C program to represent sparse matrix using array and perform sparse matrix addition, simple & fast transpose.

2. Write a C program to check the parenthesis validity of the given expression using stack.

3. Write a C program to convert infix expression to postfix and evaluate it using stack.

4. Write a C program to implement Circular Queue using array and perform add and delete operations on it.

5. Write a C Program to create a database (such as employee, student) using single linked list with options like Create, insert, delete, modify, search, print reverse, display etc.

6. Write a C Program to represent multivariable polynomial as a circular linked list and using menu perform addition, multiplication and evaluation.

7. Write a C Program to create two sorted singly linked lists, and Merge these two lists into third list without creating a new linked list.
8. Write a C Program to create Generalized Linked List and perform copy and equality operations on it.

9. Write a C program to create a binary search tree and its inorder, preorder and postorder traversal. Also perform insertion and deletion of a node in it.

10. Write a C program to create a binary search tree and find height & number of leaf nodes with and without recursion.

11. Write a C program to create an inorder threaded binary tree and perform inorder traversals.

12. Write a C program to implement Huffman’s algorithm.

13. Write a C program to represent a given graph using adjacency array and find the shortest path using Dijkstra algorithms.

14. Write a C program to represent a given graph using adjacency list and generate a minimum spanning tree using Prims/ Kruskal’s algorithm.

15. Write a C program to create a hash table and handle the collisions using chaining with or without replacement.

16. Write a C program for implementation of simple index file and perform insertion of record, display, delete and modify operations on it.

17. Write a C program for implementation of direct access file – Insertion and deletion of a record from a direct access file chaining with or without replacement.

18. Mini project which will make use of different data structures learnt in this subject.

Note: All the assignments should be implemented in LINUX environment only. Faculty members should frame any 14 assignments from first 17 assignments.

Text Books
Reference Books

Additional Reading
CS20305::PRINCIPLES OF PROGRAMMING LANGUAGES

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: C

Objectives:
- To understand basic concepts of Object Oriented Programming.
- To understand inheritance, polymorphisms, templates, file handling.
- To understand the concept of Interfaces, Packages.
- To understand the concept of Exception Handling and Multithreading
- To understand the concept of Applets and AWT
- Mapping with PEOs: II, III, (d).

List of Practical

1. Write a C++ program to implement the concept of objects, classes, constructors, destructors, inheritance.

2. Write a C++ program to implement the concept of static polymorphism (function and operator overloading.)

3. Write a C++ program to implement the concepts of virtual function (dynamic polymorphism) and friend functions

4. Write a C++ program to use the concept of generic programming (generic functions and generic classes)

5. Write a JAVA program to implement the concept of class, constructor, instance variable & class variable.

6. Write a JAVA program to implement the concept of inheritance, interface & package.

7. Write a Java program to implement the concepts of static polymorphism (function overloading) and dynamic polymorphism (using function overriding)

8. Write a JAVA program to use multithreading

9. Write a JAVA program for file handling.
10. Write a Java program with Graphical User Interface as a
   a) Stand alone desktop application
   b) Applet

11. Develop a web page using HTML, CSS and JavaScript

12. Familiarity with other programming languages
   a) Create a family hierarchy using PROLOG
   b) Write a .m file for matrix applications using MATLAB

Text Books

Reference Books

Additional Reading
CS24301 :: VB.NET

Credits: 01  
Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: C, C++ programming.

Objectives:
- To learn the fundamental concepts of VB.NET programming languages.
- To learn to windows application development using VB.NET.
- To learn to Device application development using VB.NET.
- To learn to Web application development using VB.NET.
- To learn to database application development using VB.NET.
- Mapping with PEOs :- 3

List of Practical

1. Design a simple VB.net windows application.
2. Design a simple VB.net device application.
3. Design a simple VB.net web application.
4. Design a simple VB.net application for notepad/word pad using menu editor.
5. Design a simple VB.net application for calculator.
6. Design user interface controls.
7. Design Database application.
9. Designing AJAX application.
10. Designing Windows presentation framework application.
11. Mini project.
Text Books

Reference Books
CS24303 :: C#.NET

Credits: 01

Teaching Scheme: Laboratory 2 Hrs/Week

Prerequisites: C, C++ programming.

Objectives:
• To learn the fundamental concepts of C# programming languages.
• To learn to windows application development using C#.
• To learn to Device application development using C#.
• To learn to Web application development using C#.
• To learn to database application development using C#.
• Mapping with PEOs :- II, III, (c).

List of Practical

1. Design a simple C#.net windows application.
2. Design a simple C#.net device application.
3. Design a simple C#.net web application.
4. Design a simple C#.net application for notepad/word pad using menu editor.
5. Design a simple C#.net application for calculator.
6. Design user interface controls.
7. Design Database application.
9. Designing AJAX application.
10. Designing Windows presentation framework application.
11. Mini project.
Text Books

Reference Books
MODULE IV
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CS21102:: MICROPROCESSOR AND INTERFACING

**Credits:** 03  
**Teaching Scheme:** - Theory 3 Hrs/Week

**Prerequisites:** Nil

**Objectives:**
- To explore internal architecture of microprocessor, signal and interface with memory and I/O devices.
- Mapping with PEOs: I, II, (b).

**Unit I**  
8086 Microprocessor  
(8+1 Hrs)

A. Introduction to 80x86 microprocessor, Internal Architecture, Generation of physical address, Minimum & Maximum Mode, Ready and Reset pin significance, study of 8086 supporting chips 8282(Latch), 8284(Clock Generator), 8286(Transreceiver), 8288(Bus Controller). Timing Diagram Read Write Machine Cycles, Real Mode, General Purpose Instructions.
B. Instruction Set

**Unit III**  
Assembly Language Programming & Interrupt Structure  
(8+1 Hrs)

A. Address Translation, Addressing Modes, Introduction to Assembly Language Programming, and Examples on Programming, Interrupt Structure, Interrupt service Routine, InterruptVector Table, Hardware and Software Interrupts, INTR, NMI, InterruptResponse, Execution of an ISR, Priority of Interrupts.
B. Examples on Assembly Language Programming

**Unit IV**  
Interfacing with 8086 – I  
(8+1 Hrs)

A. 8259(Programmable Interrupt Controller): Features, Block Diagram, Control & status registers, Interfacing & Programming Programmable peripheral interface (8255)-block diagram, control word, interfacing ADC, DAC.
Programmable Interval timer/counter (8253/8254)-block diagram, control word & interfacing, Mode0, Mode1, Mode3 of timer,
B. Interfacing of stepper motor, seven segment display, (8255), Mode2, Mode4, Mode5 of 8253.

Unit IV
Interfacing with 8086 – II

A. 8251 (USART): Features, Block Diagram, Control & status registers, Operating modes, Interfacing & Programming.
Keyboard debouncing, display refreshing cycle, Keyboard display interface (8279)-Block Diagram, encoded & decoded mode, Interfacing & programming.

B. Programming of 8251, Programming of 8279.

Unit V
Protected Mode Architecture


B. Address translation from Logical-Linear-Physical.

Text Books

Reference Books

Additional Reading
2. Intel Pentium Manual

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
CS21104:: MATHEMATICAL TRANSFORMS AND APPLICATIONS

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Engineering Mathematics- I and Engineering Mathematics - II

Objectives:

• To understand the linear differential equations and apply it for computer engineering.
• To understand various transformations like Laplace, Fourier, Z-transform.
• To understand concepts of vector differentiation and integration theorems.
• Mapping with PEOs: I, II, (a).

Unit I (8+1 Hrs)
Linear Differential equations of higher order

B. Coupled circuits by matrix method

Unit II (8+1 Hrs)
Complex Variables

A. Complex differentiation, Analytical functions, Cauchy-Riemann equations, Complex Integration, Cauchy’s Integral Theorem and formula, Residue Theorem and applications to Engineering Problems, Power series, Taylor series, Laurent series, Radius of convergence.

B. Bilinear Transformations and Conformal mapping.

Unit III (8+1 Hrs)
Laplace Transform

B. Application of Laplace transform to simultaneous differential equations.

**Unit IV**  
(8+1 Hrs)  
**Fourier Transform and Z Transform**

A. Introduction to signals and their properties, mathematical operations on signals, Complex Fourier series and frequency spectrum, Fourier integrals, Fourier cosine and sine transforms, Fourier transforms, properties of Fourier integral, Introduction to Z Transform, properties of Z-Transform, Inverse Z-Transform, application of Z-transform to difference equations.

B. Introduction to Discrete Fourier transform. Definition, properties and its inverse.

**Unit V**  
(8+1 Hrs)  
**Vector Analysis**

A. Vector differentiation, gradient, curl, divergence, directional derivative, line integral, independence of path and conservative vector field, Green’s theorem, Stoke’s theorem, Gauss divergence theorem.

B. Applications of vectors, Maxwell’s equations.

**Text Books**

**Reference Books**
CS21106:: DIGITAL ELECTRONICS AND LOGIC DESIGN

Credits: 03
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To learn basics of digital electronics and its practical application.
- Mapping with PEOs :- I,II, (b).

Unit I
Number system and Codes


B. Minimization of POS forms using K- Map, Quine-McCluskey Method, Half adder and subtractor, full adder and subtractor.

Unit II
Combinational & Sequential logic circuits


B. ALU 74181, BCD-to-7-segment Decoder, adder with look ahead carry generator.
Unit III
Design of sequential circuits

A. Moore/Mealy M/c’s: representation techniques, state diagrams, state tables, state reduction, state assignment, implementation using flip-flops. Applications like sequence generator and detector. Shift register (modes of operation), 4 bit bi-directional universal shift register, application of shift registers (Ring counter, Sequence generator, Johnson's counter.)ASM charts, notations, design of simple controller, multiplexer controller method, RTL notations and implementation.
B. Examples on ASM, RTL.

Unit IV
Logic Families

A. Characteristics of Digital ICs: Speed, Power dissipation, fan-out, current and voltage parameters, noise margin, operating temperature etc., TTL: Operation of TTL NAND gate, Standard TTL, TTL Characteristics, Active pull-up, Wired-AND, totem pole, open collector, Unconnected Inputs. CMOS Logic: CMOS Inverter, CMOS NAND and NOR, CMOS characteristics. Wired-logic, Unconnected Inputs, Open-Drain Outputs, Comparison of TTL and CMOS, interfacing TTL to CMOS
B. Interfacing CMOS to TTL, Tri-state logic: tri-state buffers, inverters, Study of Data sheets of 7400 Series ICs: (Basic and Universal logic gates)

Unit V
Programmable Logic Devices

A. Programmable Logic array: Input, Output Buffers, AND, OR, Invert/Non-Invert Matrix, Programming the PLA, Applications of PLAs to implement combinational and sequential logic circuits Introduction to : FPGA, CPLD.
Introduction to VHDL: Modeling Digital systems, modeling languages, modeling
B. Comparison of FPGA and CPLD, VHDL Programs.

Text Books

Reference Books
CS20108:: COMPUTER ORGANIZATION

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To understand the structure, function and characteristics of computer systems
- To understand the design principle of the various functional units of digital computers
- To understand concepts of Parallel Processing.
- Mapping with PEOs: I, II, (b).

Unit I  (8+1 Hrs)
Structure of a Computer System

B. PCI Bus. IEEE standards for Floating point representations (Double Precision Format).

Unit II  (8+1 Hrs)
Processor Organization (CPU)

A. CPU Architecture, Register Organization, Instruction types, Types of operands, Instruction formats, addressing modes and address translation. Instruction cycles, Instruction Pipelining, Pipelining Hazards, RISC Processors: RISC- Features, CISC Features, Comparison of RISC & CISC Superscalar Processors: Overview.
B. 8086 CPU, 8086 Addressing Modes, ALU organization, instruction types.

Unit III  (8+1 Hrs)
Control Unit

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011

B. Multiple-bus organization. Applications of micro programming.

Unit IV (8+1 Hrs)
Memory Organization


B. Secondary Storage: Magnetic Disk, Optical memory, CDROM, RAID

Unit V (8+1 Hrs)
Multiprocessor Configurations


B. Parallel processing applications.

Text Books


Reference Books

CS21202:: MICROPROCESSOR AND INTERFACING

Credits: 01

Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: : Nil

Objectives:
• To study interfacing of 8086, assembly language programming, DOS commands.
• Mapping with PEOs :- I, II, (b).

List of Contents

A TERM-WORK containing the record of the following:

Assignments :

1. Understanding of different Memory Models

2. List various assembler directives,

3. Understand concepts of editor, assembler, linker, loader.

4. 8086 assembly language programming, to understand the basic concepts of various functions(01,02,08,09,0A) of INT 21h

5. List various debugging commands.

6. Interface 8086 microprocessor with 4KB RAM in minimum mode. Apply memory banking. Draw memory address map and explain address decoding logic.

7. Interface 8086 microprocessor with 16KB ROM in maximum mode. Draw memory address map and explain address decoding logic.

8. Design specified time delay (delay time calculation).

9. Near, Far procedures (string example).
10. Use of string instructions

11. Study of Mother Board

Note: Students should perform vi & vii assignments on drawing sheet

Text Books

Reference Books
CS21204:: MATHEMATICAL TRANSFORMS AND APPLICATIONS

Credits: 01

Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: Syllabus covered unit-wise in theory lectures.

Objectives:
Upon completion of this module students will be able to:

- Recognize mathematical structures in practical problems
- Translate problems into mathematical language and analyze problems using methods from all the units
- Mapping with PEOs: I, II (a).

List of Contents

In this module students will work on problems to practice and apply methods introduced in the theory lectures. Discussions of problems in small groups is always encouraged and facilitated. Students are asked to submit weekly home work assignments and provide them immediate feedback and support materials.

Tutorial No. 1: Summary on higher order linear differential equations, solution of homogeneous and non homogeneous equations, complementary solution.

Tutorial No. 2: Summary on particular solution by method of variation by parameters Method of undetermined coefficients and problems solving.

Tutorial No. 3: Summary on Euler – Cauchy and Legendre Equation, simultaneous equations and problems solving.

Tutorial No. 4: Summary on Functions of complex variables, Differentiation of functions of complex variables, Analytic functions, Harmonic functions, Harmonic conjugate.

Tutorial No. 5 Summary on Integration of functions of complex variables, integration along a path, Cauchy’s theorem, Cauchy’s integral formula, Cauchy’s residue theorem and problems solving.
Tutorial No. 6:  Power Series, Taylor Series, Laurent Series

Tutorial No. 7:  Summary on Laplace transform, Laplace transform of standard functions, Properties of Laplace and problems solving.

Tutorial No. 8:  Summary on Properties of Laplace transform, Laplace transform of Unit step function, Dirac Delta function, Periodic functions and problems solving.

Tutorial No. 9:  Summary on Inverse Laplace transform, properties of inverse Laplace transform, solution of differential equations by Laplace transform method and problems solving.

Tutorial No. 10:  Summary on Fourier series, Complex form of Fourier series, Fourier integral representation, Sine and Cosine representations and problems solving,

Tutorial No. 11:  Summary on Fourier transform, Sine transform, Cosine transform and corresponding inverse and problems solving.

Tutorial No. 12:  Summary on Summary on Z transform, properties of Z transform, inverse Z transform, methods of solution and problems solving, solution of difference equation by Z transform.

Tutorial No. 13:  Summary on Vector Differentiation, problems on gradient, curl, divergence and directional derivative, summary on Vector Integration, Problems on Line Integral, Green’s Theorem.

Tutorial No. 14:  Problems on Stoke’s theorem, Gauss Divergence theorem and applications of vectors.

Text Books

CS21302:: MICROPROCESSOR AND INTERFACING

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Nil

Objectives:
• To study programming model of 8086 and assembly language programming.
• Mapping with PEOs :- I, II, (b).

List of Practical

Group A: - (Any 6)

1. Study of 8086 Architecture and Execution of sample programs.

2. Write 8086 Assembly language program to access marks of 5 subjects stored in array and find overall percentage and display grade according to it.

3. Write 8086 ALP to perform block transfer operation. (Don’t use string operations) Data bytes in a block stored in one array transfer to another array. Use debugger to show execution of program.

4. Write 8086 ALP to find and count negative number from the array of signed number stored in memory.

5. Write 8086 Assembly language program (ALP) to arrange the numbers stored in the array in ascending as well as descending order. Assume that the first location in the array hold the number of elements in the array and successive memory location will have actual array elements. Write a separate subroutine to arrange the numbers in ascending and descending order. Accept a key from the user.
   a. If user enters 0, Arrange in ascending
   b. If user enters 1, Arrange in descending

6. Write 8086 ALp to convert 2_digit HEX number into equivalent BCD number.

7. Write 8086 ALP to convert 2_digit BCD number into equivalent HEX number.
Group B: - (Any 6)

8. Write 8086 Assembly language program (ALP) for following operations on the string entered by the user.
   a. Concatenation of two strings
   b. Find number of words, characters

9. Write 8086 ALP to convert an analog signal in the range of 0V to 5V to its corresponding digital signal using successive approximation ADC.

10. Write 8086 ALP to interface DAC & generate following waveforms on oscilloscope. Comment on types of DAC’s and write detailed specifications of the DAC used
   i) Square wave -- Variable Duty Cycle & frequency.
   ii) Stair case wave
   iii) Triangular wave

11. Write 8086 ALP to rotate a stepper motor for
   a. one clockwise rotation
   b. one anti clockwise rotation

Write routines to accelerate and de-accelerate the motor
Modify your program to rotate stepper motor for given angle and given direction.

12. Write 8086 ALP to program 8253 in Mode 0. Generate a square wave with a pulse of 10 mS.

13. Write 8086 ALP to initialize 8279 & to display characters in right entry mode. Provide also the facility to display “SECOMP”/
   a. Character in left entry mode
   b. Rolling Display
   c. Flashing Display

14. Perform an experiment to establish communication between two USART’s. Initialize USART-A in asynchronous transmitter mode and interface USART-B by initializing it in asynchronous receiver mode.

Note: - Students should perform any 6 assignments from group A and any 6 assignments from group B
Text Books

Reference Books
CS21306:: DIGITAL ELECTRONICS

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Nil

Objective:
- To build and verify simple combinational and sequential circuits.
- Mapping with PEOs : - I, II, (b).

List of Practical

I Combinational Logic Design
1. Verification of Logical Gates and Boolean Algebra.

2. Code converters, e.g. Excess-3 to BCD and vice versa using logical gates.

3. Multiplexer - e.g. 16:1 Mux using 4:1 Mux (IC 74153).

4. Decoder – e.g. 2 bit comparator (IC 74138).

5. BCD adder –using IC 7483

II Sequential Circuit Design (Any six)
1. Conversion of flip-flops. e.g. JK to D, T.

2. Ripple (asynchronous) mod –N counter using J-K F-F.

3. Ripple (asynchronous) mod –N counter using IC 7490.


5. Sequence generator using JK flip-flop

6. Pseudo random number generator using 74194.(universal shift register)

7. Sequence detector (Moore ckt) using JK flip-flop
8. Sequence detector (Mealy ckt) using JK flip-flop

III ASM (Any one)
2. Design of simple combinational circuit: half adder and subtractor using VHDL language.

Text Books

Reference Books

Additional Reading
CS24302:: ASP.NET

Credits: 01
Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: C#.NET or VB.NET

Objectives:
- To learn the fundamental concepts of ASP.NET.
- To learn to Web application development using ASP.NET.
- To learn to on line database application development.
- To learn to apply security to web applications.
- Mapping with PEOs :- II,III,(c).

List of Practical

1. Design simple web application using ASP.NET.
2. Design web application with different validations.
3. Design on line database application.
4. Design data report application.
5. Design web application for uploading files on web.
6. Design AJAX application.
7. Design localized web application.
8. Design WPF browser application.
9. Authentication and authorization in asp..
10. Deployment and publishing web sites.
11. Mini project.
Text Books

Reference Books
CS24304:: Python

Credits: 01
Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: C, C++ programming.

Objectives:
• To learn the fundamental concepts of python languages.
• To learn to GUI application development using python languages.
• To learn to use regular expression for text processing in python language.
• Mapping with PEOs :- II, III, (c)

List of Practical
1. Design a Simple Python Script.
2. Design a Simple Python Script using conditional / loop constructs.
5. Design a Simple Python Script for file handling.
6. Design a Simple Python Script with exception handling.
7. Design a Simple Python Script using regular expressions.
8. Design a Simple Python Script to explore object oriented features.
10. Design a Simple Python Script with event handling.
11. Mini Project

Text Books

Reference Books
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CS30101:: OPERATING SYSTEMS

Credits: 03  Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Data Structures and Algorithms, Computer Organization.

Objectives:
- Identify the role of operating systems and explain the different structures operating systems.
- Describe OS support for processes/threads, and virtual memory, I/O and file systems.
- Evaluate processes and/or threads synchronization mechanisms and explain deadlock conditions and ways to resolve them.
- Identify the different design and implementation concepts for Unix/Linux
- Use Inter-Process Communication techniques under Unix/Linux.
- Mapping with PEOs: II, III, (d).

Unit I  (8+1 Hrs)
Introduction to OS

A. Architecture, Goals & Structures of O.S., Hardware Abstraction Layer, Basic functions, Interaction of OS and Hardware Architecture, System Calls & OS Services, Batch, Multiprogramming, Multitasking, Time sharing, Parallel, Distributed & Real-time OS.
Overview of 386 programming model, Introduction to Mobile OS: Architecture & Mobile OS Layered Model of Mobile OS.
Examples of OS: Linux, MS-Windows 2000.
B. Variants of Linux

Unit II  (8+1 Hrs)
Process Management


Structure & Syllabus of  B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
OS Services layer in the Mobile OS: Generic Services.

B. Message Passing, Sleeping Barber problem.

Unit III (8+1 Hrs)  
Scheduling and Deadlock


OS Services layer in the Mobile OS: Comms Services.

B. Thread Scheduling, Real Time Scheduling.

Unit IV (8+1 Hrs)  
Memory Management

A. Memory Management requirements, Memory Partitioning: Fixed and Variable Partitioning, Memory Allocation: Allocation Strategies (First Fit, Best Fit, and Worst Fit), Fragmentation, Swapping.

Virtual Memory: Concepts, Segmentation, Paging, Address Translation, Page Replacement Policies (FIFO, LRU, Optimal, Other Strategies), Thrashing.

OS Services layer in the Mobile OS: Multimedia and Graphics Services, Connectivity Services.

B. Demand paging, Working Set Model.

Unit V (8+1 Hrs)  
I/O Devices, Files & Shell Programming

A. I/O management & Disk scheduling: I/O Devices, Operating System design issues, I/O Buffering, Disk Scheduling (FCFS, SCAN, C-SCAN, SSTF), RAID.


Shell and Command Programming, AWK Programming.

B. Organization of I/O functions, Disk Caches, Secondary Storage Management.

Text Books


Reference Books

Additional Reading
CS30103:: COMPUTER NETWORKS

Credits: 03  Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Data Communication.

Objectives:
- To understand some of the common data link layer protocols.
- To learn various routing algorithms.
- To understand various protocols used at different layers.
- Mapping with PEOs :- II, III, (d).

Unit I
Data Link Layer
(8+1 Hrs)


B. Switched, fast and Gigabit Ethernet.

Unit II
Medium Access Control
(8+1 Hrs)

A. Channel allocation: Static and Dynamic allocation, Multiple Access Protocols: ALOHA, CSMA, Collision-free and limited-contention protocols, WDMA. Ethernet: Cabling, MAC sub-layer protocol, Logical link control, Wireless LAN, Broad band wireless, Bluetooth.

B. Switched, fast and Gigabit Ethernet.

Unit III
Network Layer
(9+2 Hrs)

A. Design Issues, Packet switching, Connectionless and Connection-oriented Services, Virtual Circuit and Datagram Subnets, Autonomous system.

Routing Algorithms: Optimality principle, shortest path routing, flooding, Distance Vector routing, link state routing, hierarchical routing.

Congestion Control and QOS: General Principles, Congestion prevention policies, Load shading, Jitter Control, Quality of Service, Internetworking.

B. Routing Algorithms: Broadcast routing, Multicast routing, Routing for mobile hosts.

Unit IV (8+1 Hrs)

Transport Layer


B. Sockets and Socket programming in Linux and Windows.

Unit V (7+1 Hrs)

Application Layer

A. Domain Name System (DNS) and DNS servers, MIME, SMTP, Mail Gateways, Remote login, File Transfer Protocol, SNMP.

Cloud computing: Architectures, working principle.


Text Books

Reference Books

Additional Reading
CS30109 :: WEB TECHNOLOGIES

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Principles of Programming Languages.

Objectives:
- To understand different web technologies like XML, XSL, DOM, Servlet, AJAX, Web 2.0, Web 3.0 etc.
- To understanding the Servlet model.
- To designing and developing Servlet using session management.
- To understanding the java server pages (JSP) technology model.
- Mapping with PEOs :- IV, (i).

Unit I
HTML and Client Side Technologies
(8 Hrs)
A. Introduction to web, HTML, HTML formatting Tags, Tables, Frames, Introduction to VBScript, JavaScript, JavaScript Objects, JavaScript Cookies, JavaScript Browser Functions.
B. Use of Timing and Dynamic objects in JavaScript

Unit II
The Document Object Model
(8 Hrs)
B. Referring DOM using VBScript /JavaScript.

Unit III
XML, Style sheet (XLS) & CSS
(8 Hrs)
A. Introduction to XML, Components of XML; Parsing XML: Parsing Methodologies, DTD, SAX API, The Java API for XML parsing (JAXP), Overview, XSLT, Referring XLS style sheet, CSS.
B. X-Path, X-query.
Unit IV
Java Servlet and JSP

A. Servlet Architecture, Servlet Interface, Servlet HTTP Interface, Request Processing, Response Generation, Session Management, Servlet Deployment, Servlet Configuration, Servlet Service Management. JSP Overview, JSP Language Basics, JSP Translation and Compilation Directives, Java Scripting from JSP, Java Abstraction of JSP, Standard Java Objects from JSP, Standard Java Action from JSP, JSP Configuration and Deployment, Custom Java Actions and Tags from JSP.

B. Session and Cookies management in Servlet and JSP.

Unit V
Emerging Trends in Web Technology

A. AJAX Fundamentals, PHP, Designing Dynamic GUI using SVG. Introduction to HTML 5.0, MathML, Introduction to web 2.0, web 3.0, RSS Feeds.

B. Design Social networks/Blogs web site.

Text Books

Reference Books

Additional Reading
1. “DOM Level 1 Specifications” online at www.w3.org/TR/REC-DOM-Level-1/
CS30201:: OPERATING SYSTEMS

Credits: 01  Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites:: Data Structures and Algorithms, Computer Organization.

Objectives:
- Understand Operating System concepts.
- Be familiar with Basic and Advance Unix commands.
- Be able to write Shell and AWK scripts.
- Understand and solve problems involving process control, mutual exclusion, deadlock and synchronization.
- Shell programming in Unix.
- Mapping with PEOs :- II, III, (d).

List of Contents

A TERM-WORK containing the record of the following:

1. Execution of Advance Unix commands.

2. Write a shell program to sort an array of numbers using any sort method.

3. Execution of AWK related commands.


5. Solve the Producer-Consumer problem using Threads and Mutex.

6. Solve the Producers-Consumers problem using Threads and Semaphores.


8. Draw the Gantt charts and compute the finish time, turnaround time and waiting time for the following algorithms:
   a. First come First serve
   b. Shortest Job First (Non-Preemptive)
9. Calculate the number of page faults for a reference string for the following page replacement algorithms:
   a. Optimal
   b. FIFO

10. Calculate the total distance traversed by the disk arm to satisfy the pending requests for the following disk scheduling algorithms:
    a. FCFS
    b. C-SCAN

Text Books

Reference Books

Additional Reading:
CS30203:: COMPUTER NETWORKS

Credits: 01

Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: : Nil

Objectives:

- To understand the analysis of network
- Understand the design of network.
- Mapping with PEOs :- II, III, (d).

List of Contents

A TERM-WORK containing the record of the following:

A. Assignments :
   i. Study of web Server.
   ii. PC-to-PC communication using RS-232.
   iii. Install two LAN Cards in one of the machine. Install and study router.
   iv. Introduction to NS-2/NS-3/OMNET
   v. Study of System Socket Calls.

B. Mathematical Analysis
   i. Problems on propagation delay
   ii. Problems on transmission delay
   iii. Problems on latency using queuing theory.

Text Books


Reference Books

Additional Reading

CS30301:: OPERATING SYSTEMS AND COMPUTER NETWORKS

Credits: 01               Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Data Structures and Algorithms, Computer Organization

Objectives:
- Be familiar with implementation of a multiprogramming operating system.
- Be familiar with implementation of paging, scheduling.
- Understand the design of computer network.
- Student must be able analyze the packets and write the socket programming.
- Mapping with PEOs :- II,III, (d).

List of Practical
Part A:

1. Implementation of a multiprogramming operating system:
   a. Stage I:
      i. CPU/ Machine Simulation
      ii. Supervisor Call through interrupt
   b. Stage II:
      i. Paging
      ii. Error Handling
      iii. Interrupt Generation and Servicing
      iv. Process Data Structure
   c. Stage III:
      i. Multiprogramming
ii. Virtual Memory

iii. Process Scheduling and Synchronization

iv. Inter-Process Communication

v. I/O Handling, Spooling and Buffering

**Part B:**

1. Study of existing LAN and understand the design and various components. Set up a small network of 3 to 4 computers and Hub/Switch as directed by the instructor. Use Lan Card, UTP Cables and Connectors. Install LAN Cards and Crimp the connectors. Assign unique IP addresses and share C drive on each machine. Test the network by using PING command. Use protocol analyzer Software.

2. Study any protocol analyzer software (eg. LanExplorer) to learn and use its important features. Study of network monitoring software like ETHREAL software. Assignment to examine TCP/IP and non-TCP/IP protocols (IPX/SPX) and capture them using protocol analyzer Software.

3. Program to implement sliding window protocol

4. TCP Socket programming in Linux

5. UDP Socket programming in Linux

6. Mini Project.

**Text Books**


**Reference Books**


**Additional Reading**


Structure & Syllabus of B.E.(Information Technology) Program-Pattern ‘E11’, Issue 03, Rev01 dated 02/04/2011
CS30309 :: WEB TECHNOLOGIES

Credits: 01  
Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Principles of Programming Languages

Objectives:
- To understand different web technologies like XML, XSL, DOM, Servlet etc.
- To understanding the Servlet model.
- To designing and developing Servlet using session management.
- To understanding the java server pages (JSP) technology model.
- Mapping with PEOs :- IV, V, (i).

List of Practical

1. Design HTML forms.
2. Design dynamic web page with DOM and client side script.
3. Design a XML database with DTD.
4. Design a Simple Servlet application.
5. Design a Simple Servlet application with database.
6. Design a Simple JSP application.
7. Design a Social Networking web application.
8. Design a Blog application with RSS feeds.
9. Design an AJAX application with SVG graphics.
10. Design an Web service for any application.
11. Mini Project

Text Books


Reference Books

MODULE VI
## Structure, T.E. (Module VI)

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CS 30102:: SOFTWARE ENGINEERING

Credits: 03  Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Data Structures

Objectives:
- To learn the complete Software life cycle and understand its major activities such as software requirement analysis, design, testing, and implementation.
- Understanding and Experience in Writing Requirements and Specifications.
- To thoroughly understand the practices of analysis and design (OOA and OOD)
- To understand the relative merits of the different UML diagrams
- Transforming analysis into design and relate it to implementation model
- Mapping with PEOs :- III, (e)

Unit I (8+1 Hrs)
Software Process Models and OO Methodologies

B. Overview of OO Methodologies: OOAD, OOSE, OMT, DSDM

Unit II (8+1 Hrs)
Requirement Engineering and Model Driven Development

Overview of Model Driven Development, Introduction to Model Driven Architecture: MDA Terms and Concepts, Model Mappings, Marking Models, Executable Models, MOF and XMI, Introduction to UML Metamodel
B. Programming In Small Versus Programming In Large, Extensibility Mechanisms and its usage, Introduction to OCL, UML 2.0 Diagram set

Unit III
System Behavior Analysis

A. Static Behavior: Use Cases, Use Case Diagram Components, Use Case Diagram, Actor Generalization, Include and Extend, Template for Use Case Narrative, Using Use Cases, The Domain Perspective, Data Dictionary: Finding the Objects, Responsibilities, Collaborators, and Attributes, CRC Cards, Class Models and Use Case Models, Judging the Domain Model, Capturing system behavior in use cases
Dynamic Behavior: Sequence diagrams, object lifelines and message types, Modeling collections multiobjects, Refining sequence diagrams, Collaboration diagrams, States, events and actions, Nested machines and concurrency, Modifying the object model to facilitate states, Modeling methods with activity diagrams, Activity Diagrams: Decisions and Merges, Synchronization, Iteration, Partitions, Parameters and Pins, Expansion Regions, Swimlanes, concurrency and synchronization
B. Study of other Behavioral Diagrams: Communication Diagram, Interaction Overview Diagrams, Timing Diagrams

Unit IV
System Design Engineering

A. Design quality, Design Concepts, The Design Model, Introduction to Pattern-Based Software Design, Architecture styles, Reference Architectures Architectural Design: Software Architecture, Data Design and Architectural Design, Design of Software Objects, Features and Methods, Cohesion and Coupling between Objects, Coupling and Visibility, Interfaces, Interfaces with Ball and Socket Notation, Templates, Analysis model vs. design model classes, Categorizing classes: entity, boundary and control, Modeling associations and collections, Preserving referential integrity, Achieving reusability, Reuse through delegation, Identifying and using service packages, Improving reuse with design patterns

Unit V
System Implementation and Project Management

A. Packages and interfaces: Distinguishing between classes/interfaces, Exposing class and package interfaces, Component and deployment diagrams: Describing dependencies,
Deploying components across threads, processes and processors

Text Books

Reference Books

Additional Reading
CS30104:: COMPUTER GRAPHICS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
• To understand basic concepts of computer graphics.
• To understand algorithms to draw various graphics primitives.
• To understand 2-D and 3-D transformations.
• To understand hidden surface removal, light, color, shading
• To understand curve and fractals.
• Mapping with PEOs :- I, II, (b).

Unit I  (8+1 Hrs)
Basic Concepts

A. Graphics Primitives: Introduction to computer graphics, Display adapters, Display modes, Pixel, Frame Buffer, Display file structure, Display file interpreter, Raster scan & random scan displays, Aliasing and Antialiasing.
Mathematical foundations: Lines and line representations, Vector and affine spaces, Polygons and polygon interiors, Dot and cross products, Planes and plane representations, Line-line and line-plane intersections, Homogeneous coordinates, Normalized Device Coordinates
Scan conversions: DDA and Bresenham’s line and circle drawing algorithms, Curve functions, Midpoint circle algorithm.
B. Display devices, Interactive devices, Data generating devices, Thick lines.

Unit II  (9+1 Hrs)
Polygons

A. Polygons: Introduction, Types of polygons, Inside-outside test of polygon, Polygon filling: Seed fill, Boundary fill, Edge fill, Scan line fill algorithm.
B. Fence fill, Generalized Polygon clipping.
Unit III  
(9+1 Hrs)
Geometric Transformations


3D Transformations: Introduction, 3D point representation, 3D maths, Left handed system, Right handed system, Scaling, Rotation, Translation, Matrix representation, Derivation of Rotation matrices along the main axis, Rotation about an arbitrary axis, Euler angles, Quaternion.


B. Problems based on 2D and 3D transformation, 3D reflection, 3D clipping.

Unit IV  
(7+1 Hrs)
Hidden Surfaces, Light, Color and shading


Light, Color and Shading: Introduction, Line vs rays, Lighting properties, Light Illumination(Diffuse, Ambient, Specular), Point source illumination, Shading Algorithms (Phong, Gourad), Human vision and color, RGB Color Model.

B. Scan line algorithm for Depth Comparison, CMY and HSV color model, shadows.

Unit V  
(7+1 Hrs)
Curve Design and Fractals

A. Introduction, Curve generation, Curve continuity, Conic curves, Piecewise curve design, LeGrange interpolated curves, Spline curve representation, B Spline Curves, Non Uniform Rational B Spline curves, Fractals, Hilbert curve, Triadic Koch Curve, Fractal

B. Bezier curves, Fractal surfaces.

Text Books


Reference Books

Additional Reading
CS30106:: DATABASE MANAGEMENT SYSTEMS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Data structures

Objectives:
- To understand importance of Database Management System (DBMS) over traditional file processing system.
- To understand significance of requirement analysis phases in designing entity relationship data model.
- To apply normalization process to design a relational model in the required normal form.
- To use SQL to create database objects, populate tables, and retrieve data.
- To understand the concept of transaction and the implementation of transaction management process.
- Mapping with PEOs: II, III, (d).

Unit I  (8+2 Hrs)
Introduction to DBMS and ER Data Model

A. Data Storage: File processing system, Disadvantages; DBMS: Need of DBMS; Data, Database, Database System, Database Management System, Metadata, Data Dictionary; Data Abstraction, Data Independence; System Architecture of DBMS; Data Model, Data Requirements Analysis; ER Model: Entity, Entity Set, Attributes, Primary Key, Relationship Set, Degree and Attributes of Relationship, Role, Mapping Cardinality, Participation Constraint, Weak Entity Set, Extended ER Features.
B. Object Oriented, Object Relational, Hierarchical and Network Data Models, Tools for Designing ER Model, Introduction of Popularly used Relational DBMS Packages.

Unit II  (8+2 Hrs)
Relational Data Model

A. Relation, Schema, Attributes, Tuples, Domains, Relation Degree (or Arity), Cardinality, Relation Intention and Extension; Super Key, Candidate Key, Primary Key and Foreign Key; Relational Model Constraints; Schema Diagram, ER to Relational Mapping; Formal Relational Query Languages: Relational Algebra, Tuple Relational Calculus.
B. Domain Relational Calculus, Codd's Twelve Rules for Relational DBMS, Life Cycle of a Relational Database, Reverse Engineering: Conversion of Relational model into ER/EER Model.

Unit III (8+2 Hrs)
Normalization

A. Normalization: Anomalies of un-Normalized Relation; Functional Dependency: Trivial, Full, Partial, Transitive, Multivalued Dependency; Dependency Diagram; Inference Rules; FD Closure; Algorithms for 1. Candidate Key, 2. Attribute Set Closure, 3. Minimal Cover; Decomposition Properties; Normal Forms: 1NF, 2NF, 3NF, BCNF, 4NF; Bottom - up Approach for Relational Model Design, Relational Synthesis Algorithm; Normalization Trade off

B. Join and Inclusion Dependency, 5NF, DKNF, Normalization at Conceptual Level.

Unit IV (8+2 Hrs)
Structured Query Language (SQL)

A. Introduction; DDL, DML, TCL, DCL; Advantages and Disadvantages of SQL; DDL: Create, Drop, Alter Database Objects (Table, Constraints, View etc.); SELECT Queries: Simple Queries, Set Membership, Set Operations, Aggregate Functions, Group-by, Having Construct, Join Types, Nested Subqueries; PL/SQL: Block, Cursor, Procedure, Function, Trigger; Embedded SQL.

B. DCL-Security and Authorization, Date-Timestamp, String and Numerical Functions, Mapping of Relational Algebra to SQL.

Unit V (8+2 Hrs)
Transaction Management

A. Transaction: Concept, ACID properties, States; Schedule: Definition, Types, Conflict and View Serializability, Precedence Graph, un/Recoverable, Cascadeless Schedules; Concurrency Control Protocols: Lock Based, Timestamp Based Protocol; Deadlock Detection and Avoidance Techniques; Recovery System: Log Based Recovery, Checkpoints; Query Processing and Optimization

Current Trends: OLAP, Data Warehousing, Data Mining, Distributed databases, Parallel databases.

B. Tree and Multiversion Protocol for Concurrency Control, ARIES Recovery Technique, Shadow Paging; Information Retrieval System
Outcomes:
1. Understanding of entity relationship data model and relational data model design
2. Students will be able to design normalized relational model
3. Students will have understanding of SQL and PL/SQL constructs

Text Books

Reference Books

Reference Books

CS30110:: MANAGEMENT INFORMATION SYSTEMS

Credits: 03 Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
• To identify and analyze the role of computer based information systems in the successful management of organizations.

• Elaborate the major types of information systems in organizations and their role in managing the organization.
To recognize the moral dimensions of information systems problems and apply specific ethical principles to determine an appropriate course of action.

Mapping with PEOs: III, IV, (f).

Unit I  
Foundations of Information Systems  
(9+1 Hrs)

B. Selection of a Domain: Banking, Healthcare, Hotel, Telecom, Education, Agriculture, Shopping Mall, Automobile, Food Industry etc.

Unit II  
Manufacturing and Service Systems  
(7+1 Hrs)

B. Identification of Functional Levels, Services and Products in Selected Domain.

Unit III  
e-Business  
(8+2 Hrs)

B. Study of Process to accommodate e-Business Approach in Selected Domain.

Unit IV  
Information Systems for Decision Support  
(8+2 Hrs)

A. Business and Decision Support, Decision Making Process, Components of DSS, MIS, Difference between DSS and MIS, Online Analytical Processing, Types of DSS, Using DSS, What-if analysis, Sensitivity analysis, Goal-seeking analysis, Optimization analysis, Data Mining for Decision Support, Executive Information Systems, Knowledge Management Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011

B. Identify Decision-making Aspects in a Selected Domain with Appropriate Examples.

**Unit V**

**Challenges Ahead**

(8+2 Hrs)


**B.** Study of Cybercrimes and Preventive Measures w. r. t. Selected Domain.

**Text Books**


**Reference Books**


**Additional Reading**

CS 30202:: SOFTWARE ENGINEERING

Credits: 01

Teaching Scheme: -Tutorial 1 Hr/Week

Prerequisites: Data Structures

Objectives:

• To learn the complete Software life cycle and understand its major activities such as software requirement analysis, design, testing, and implementation.
• Understanding and Experience in Writing Requirements and Specifications.
• To thoroughly understand the practices of analysis and design (OOA and OOD)
• To understand the relative merits of the different UML diagrams
• Transforming analysis into design and relate it to implementation model
• Mapping with PEOs :- III, (a).

List of Contents

A TERM-WORK containing the record of the following:

1. To study modeling methodologies and identify their applicability to various categories of projects.

2. To understand Requirement Elicitation Techniques and recognize types of requirement while preparing System Requirement Specification.

3. To study MDD / MDA and identify the importance of Model Transformation.

4. To study types of MOF and metamodel concepts for various diagrams in UML 2.0.

5. To identify System Scope, Actors, Use Cases, Use Case structuring for a given problem and perform Use Case narration in template form with normal/alternate flows.

6. To identify Entity, Control, Boundary objects and trace object interactions for scenarios from use cases.
7. To identify object states, transitions, entry-exit points, concurrency, action parallelism and prepare a state chart diagram for given object scenario.

8. To prepare detailed Activity diagram with notational compliance to UML 2.0 indicating clear use of pins, fork-join, synchronization, datastores.

9. To prepare Class diagram for a defined problem with relationships, associations, hierarchies, interfaces, roles and multiplicity indicators.

10. To prepare Component and Deployment diagram for a defined problem.

Text Books

Reference Books

Additional Reading
CS30204:: COMPUTER GRAPHICS

Credits: 01

Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: : Nil

Objectives:
- To understand algorithms to draw various graphics primitives.
- To understand 2-D and 3-D transformations.
- To understand clipping, curve, fractals.
- Mapping with PEOs :- I,II, (b).

List of Contents

A TERM-WORK containing the record of the following:

B. Assignments :
  1. Write a Program to implement DDA Line drawing algorithm.
  2. Write a Program to implement Bresenham’s Circle drawing algorithm.
  3. Write a Program to implement Scan Line fill algorithm.
  4. Write a Program to implement Cohen Sutherland line clipping algorithm.
  5. Write a Program to implement Polygon clipping algorithm.
  6. Write a Program to draw a Trajdic Koch curve.
  7. Write a Program to draw a fractal line and surface.

Text Books
Reference Books

CS 30302:: SOFTWARE ENGINEERING

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Data Structures

Objectives:
- To learn the complete Software life cycle and understand its major activities such as software requirement analysis, design, testing, and implementation.
- Understanding and Experience in Writing Requirements and Specifications.
- To thoroughly understand the practices of analysis and design (OOA and OOD)
- To understand the relative merits of the different UML diagrams
  Transforming analysis into design and relate it to implementation model
- Mapping with PEOs :- III, (a).

List of Practical

1. To narrate Requirement Definition Document for the target system with following three areas:
   a. Problem Identification
   b. Problem Definition
   c. Problem Statement


3. To decompose and organize the problem domain area into broad subject areas and identify the boundaries of problem/system. Specify the behavior of the target system and map requirements to Use cases.
   The System Context Diagram depicts the overall System behavioral trace and Requirement Capture diagram depicts the hierarchical Use case Organization. The Use Case diagram should encompass
   a. Actors (External Users)
   b. Transactions (Use Cases)
   c. Event responses related to transactions with external agents.
   d. Detection of System boundaries indicating scope of system.

4. To depict the dynamic behavior of the target system using sequence diagram. The Sequence diagram should be based on the Scenarios generated by the inter-object Communication. The model should depict:
a. Discrete, distinguishable entities (class).
b. Events (Individual stimulus from one object to another).
c. Conditional events and relationship representation.

5. To depict the state transition with the life history of objects of a given class model. The model should depict:
   a. Possible ways the object can respond to events from other objects.
   b. Determine of start, end, and transition states.

6. To depict the dynamic behavior using detailed Activity diagram.

7. To develop logical static structure of target system with Class diagram. To prepare Class Collaboration-Responsibility (CRC) cards for the Conceptual classes traced from System analysis phase. The design model should depict
   a. Relationship between classes: inheritance, Assertion, Aggregation, Instantiation
   b. Identification of objects and their purpose.
   c. Roles / responsibilities entities that determine system behavior.

8. To represent physical module that provides occurrence of classes or other logical elements identified during analysis and design of system using Component diagram. The model should depict allocation of classes to modules. To narrate the Program Design Language Constructs for the target system.

9. To represent deployment view of the system through Architecture Diagram.

10. To implement the system according to specification.

Text Books

Reference Books

Additional Reading
CS30306:: DATABASE MANAGEMENT SYSTEMS

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Nil

Objectives:

- Deep understanding of database design.
- Implementation of database systems.
- Conversant with oracle database: SQLPlus, PL/SQL.
- Mapping with PEOs: II, III, (d).

List of Practical

1. Choose a database system you propose to work on throughout the course. Perform requirements analysis in detail for the same.

2. Systematic design of an entity-relationship (ER) data model for the selected database system.

3. Convert above ER model to relational model. List functional dependencies, draw dependency diagrams. Normalize these relations up to 3NF.


5. Create tables with appropriate constraints for the relational schema [designed in above assignments] using DDL queries. Create views, indices, and sequence. Alter the schema by adding/removing columns and constraints. Write DML statements to insert, modify and remove data in tables.

6. Execute ‘SELECT’ queries using various operators. Also make use of order by, group by, aggregate functions, having clause, and set operators. Use SQL single row functions for date, time, string etc.

7. Write queries involving multiple tables using equijoin, non equijoin, self join and outer join. Write queries containing single row, multiple row, correlated
subqueries using operators like =, in, any, all, exists etc. Write DML queries containing subqueries.

8. Write meaningful stored procedures in PL/SQL to populate a table. Make use of cursors and different arguments.

9. Write useful stored functions in PL/SQL to perform complex computation. Write row level and statement level triggers in PL/SQL.

10. Implement a small database application for the above system using suitable front end and back end tool. Also demonstrate use of Embedded SQL. Generate different useful reports.

Text Books

Reference Books
## Structure, B.E. (Module VII)

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CS40101:: HUMAN COMPUTER INTERACTION

Credits: 03  Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To understand the influence of the human factors on the interface design and development of application software.
- To study the methods and techniques for user-centered design and task analysis-based structural design of the interactions.
- To provide the concepts and strategies for making design decisions for user interface.
- Mapping with PEOs :- II, III, (c ).

Unit I  (7+1 Hrs)
Introduction
B. Selection of Product/System with Interface and Study of Its Usability.

Unit II  (9+2 Hrs)
Principles, Models & Guidelines
B. Specialized Users and Their Relevance to Selected Product/System.

Unit III  (8+2 Hrs)
Design Process and Interaction Styles

Structure & Syllabus of  B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011

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Form-Filling, Natural Language, Command Language, Dialog Design Notations.

B. Web/Interface Design for Selected Product/System.

**Unit IV**

**Evaluation Techniques and Implementation support**

(8+2 Hrs)


B. Usability Evaluation of Selected Product/System.

**Unit V**

**Documentation and Groupware**

(8+1 Hrs)

A. Classification of Documents, Printed Manuals, Online Facilities, Error Messages, Groupware, Goals of Co-operations, Asynchronous Interactions, Synchronous Distributed, Face to Face, Applying CSCW to Learning, Social Networks, Social Networking Sites.

B. Documentation Design for Selected Product/System.

**Text Books**


**Reference Books**


**Additional Reading**


Structure & Syllabus of B.E.(Information Technology) Program-Pattern ‘E11’, Issue 03, Rev01 dated 02/04/2011
CS41105::DATA ACQUISITION SYSTEMS

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
1. To logically translate Process Control functions into Control Instrumentation
2. To select the adequate data acquisition method for data input and find the necessary data sources.

Unit I
(8+1 Hrs)
Systems and Measurements
A. Introduction to data acquisition system(DAS), Block Diagram of DAS, Generalized measurement system, Characteristics of measuring devices, accuracy, precision, errors, linearity, hysteresis, threshold, repeatability, reliability calibration., Basic concepts of feedback control system:- open loop and closed loop, Introduction to Continuous and discontinuous control systems.
B. Case study Proportional control system.

Unit II
(8+1 Hrs)
Transducers and sensors
A. Basic principle and characteristics of following sensors and transducers :- Thermal(RTD, Thermocouple), Displacement(LVDT), optical(optocoupler, LDR)
Signal conditioning: Types (analog and digital), Application of OpAmp in signal conditioning as -amplifier, adder, sub tractor, Low pass filter, high pass filter, I-V converter, integrator, differentiator.
B. Design of signal conditioning circuits for above sensors and transducers.

Unit III
(9+1 Hrs)
Microcontroller for Data Acquisition
A. Architecture, block diagram, pin diagram, clock and oscillator, Reset Circuit and Timing Details, programming model, special function registers, PSW, stack, port Structure, I/O interfacing concepts. Instruction set, addressing modes. assembly language programming, Timer/ Counter programming, Serial communication: basics of serial communication
B. RS-232 and programming

Unit IV

Controls and Networks
(7+1 Hrs)

A. Digital controls: Simple alarms, two position controls, Multi Variable alarms, Attenuators, Interfacing Buses: RS 485, I²C, SPI, CAN

B. Interfacing Buses:- IEEE 488

Unit V

Programmable Logic Controls.
(8+1 Hrs)

A. Relay Controllers, Relay Sequencers, PLC: Design, PLC Operation: I/O scan mode, execution mode, PLC Software Functions: Timer, accumulated timer, counters. Elements of Ladder Diagrams (limit, pressure, level, thermal, mechanical switch) and Examples based on ladder diagram. Data Loggers.

B. SCADA, Case Studies of Data Acquisition Systems.

Text Books

Reference Books
CS42104:: NEURAL NETWORKS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To focus on the foundations of neural network theory and the application of
  neural network models in engineering, cognitive science, and artificial
  intelligence.
- To introduce the neural networks as means for computational learning.
- To present the basic network architectures for classification and regression.
- Mapping with PEOs :- III, IV, (f)

Unit I (8+2 Hrs)
Introduction

A. Introduction and Role of Neural Network (NN), Fundamentals of Biological Neural
   Networks, Basic Principles of ANN and their early Structures, Design of NN, NN
   models.
B. Learning Rules, Supervised, Unsupervised learning.

Unit II (8+1 Hrs)
Perceptrons

A. Basic Structure, Single-Layer Perceptron, Multilayer Perceptron, Adaline and
   Madaline, Derivation of BP algorithm, BP Case Study: Character Recognition, EXOR
   (XOR) Problem,
B. Perceptron Case Study

Unit III (8+2 Hrs)
Hopfield Networks

A. Binary Hopfield Networks, Bidirectional Associative Memory Principle, Walsh
   Functions, Network Stability, Continuous Hopfield Models, Continuous Energy
B. Hopfield Network Case Study: Character Recognition, Traveling Salesman Problem.

Unit IV (8+1 Hrs)

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
Adaptive Resonance Theory (ART)

B. Case Study: Speech Recognition

Unit V (8+2 Hrs)
Training & Recurrent Networks

B. Recurrent Back Propagation Network Case Study: Character Recognition.

Text Books

Reference Books

Additional Reading
CS42103:: PARALLEL COMPUTING ON GPU

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
1. Students will be able to detect parallelism in a given problem
2. Students can design parallel algorithm for a given problem for GPU
3. Students will be able to write parallel programs to execute on GPU

Unit I (8+1 Hrs)  
Parallel programming basics

A. Parallel programming definition, motivation, Types and levels of parallelism, Different grains of parallelism, data dependence graph, data parallelism, functional parallelism, Flynn’s classification of multi-processors, Motivation for heterogeneous programming, Definition of thread and process, programming parallel computers- extend a compiler, extend a sequential programming language, add a parallel programming layer, create a parallel language.
B. multiprocessor and multicomputer systems, interconnection networks

Unit II (8+1 Hrs)  
Introduction to GPU, architecture and parallel algorithms

A. Introduction to GPU computing, motivation, Modern GPU architecture case study: NVIDIA Fermi Tesla C2050/Kepler K20, GPU memories- global, shared, texture memory and their properties and uses, roles of CPU and GPU in parallel computing, GPU computing domain areas and success. Parallel algorithm design. Speedup and scalability.
B. parallel algorithm design for data clustering, theory of locality of reference

Unit III (8+1 Hrs)  
Compute Unified Device Architecture (CUDA)
A. CUDA Architecture, CUDA programming model, execution model, thread organization: Concept of grid, block and thread, thread index generation, warp; memory model: Introduction to global, shared, local memories, usage of cache, texture cache, constant memory, memory banks and bank conflicts, memory coalescing, CUDA structure and API details. CUDA example programs (Vector dot product, Vector-Matrix multiplication and etc).

B. atomic operations in CUDA

Unit IV (8+1 Hrs)
Problem solving using GPUs
A. Single vss double precision, solving problems that involves Vectors, Matrices, Binomial coefficients, Bernstein coefficients and etc. Instructor will choose the problems from several domains.
B. Study problems given by instructor

Unit V (8+1 Hrs)
Parallel reduction and Tools
A. Reduction operation using prefix sum example. Performance issues in algorithms-deciding parallelization of a part of algorithm and selecting the highest parallelism, Need of profilers, Introduction to CUDA Tools: MemCheck and Visual Profiler.

B. Memory leaks and associated problems

Text Books
2. CUDA by Example: An Introduction to General-Purpose GPU Programming by Jason Sanders and Edward Kandrot
3. Parallel Programming in C with MPI and OpenMP by Michael J. Quinn, Tata McGraw-Hill Edition

References:

http://developer.nvidia.com/
www tutorials on introduction to parallel computing
Other references suggested by instructor

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’, Issue 03, Rev01 dated 02/04/2011
CS42105:: MOBILE COMPUTING

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites:
- Communication Engineering
- Computer Network

Objectives
- To understand various wireless networks and their working characteristics.
- To understand characteristics of local and wide area network technologies such as Bluetooth, 802.11 and GSM.
- Distributed systems platforms for mobile computing, including proxy based architectures and service discovery and interaction platforms.
- Mapping with PEOs :- I, V, (j).

Unit I  (8+1 Hrs)
Cellular Network

B. Signal and signal propagation, IEEE 802.11 standard, 802.11a,b,g, 802.15, 802.16.

Unit II  (8+1 Hrs)
GSM Communication

A. System Architecture: GSM Radio subsystem, Interfaces, Network and switching subsystem, Operation subsystem. GSM channels, GSM protocol architecture, Location tracking and call setup, Security, Data services N/W signaling, GSM mobility management, Administration and maintenance.
Handoff- Initialization of handoff, Delaying handoff, Forcing handoff, Power different handoff. Mobile assisted handoff, Intersystem handoff.
B. Survey of GSM network, Hard, Soft Handoff.
Unit III (9+1 Hrs)
GSM Bearer Services

A. SMS architecture protocol, Hierarchy, VOIP services for mobile networks.
WAP: model and architecture, Gateway, protocol stack.
Telecommunication system: GPRS, wireless in local loop, DECT, EDGE, UMTS, Paging
systems, CDPD.
B. Wireless application environment, Bluetooth.

Unit IV (8+1 Hrs)
Mobile Network and Transport layer

A. Mobile Network layer: Mobile IP, Mobile node, IP packet delivery, Agent discovery,
Registration, Tunneling and encapsulation, Mobile ad-hoc networks.
Mobile Transport layer: Traditional TCP, Indirect TCP, Snooping TCP, Mobile TCP,
Fast retransmit/recovery, Transmission/time-out freezing, Selective retransmission,
Transaction-oriented TCP.
B. TCP over 2.5/3G wireless networks.

Unit V (7+1 Hrs)
Mobile Databases

A. Database hoarding, Data caching, Data cache and web cache maintenance in mobile
environments, Client-Server computing and adaptation, Query processing. Data recovery
process, Issues relating to quality of service, Digital audio broadcasting: DAB System,
DAB objects, Object transfer protocol, DVB: DVB system.
B. Mobile Billing

Text Books
81-297-0350-5
2. “Mobile Communication”, G.K.Behera, Lopamudra Das, Scitech
publications,ISBN -9788183711791

Reference Books
1. “Wireless and Mobile Network Architectures”, Yi-Bing Lin, Imrich Chlamtac,
568677-7
CS42102:: IMAGE PROCESSING

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To explore several different image processing techniques, and learn to improve images with them.
- To get familiarized to image enhancement and compression.
- Mapping with PEOs :- II, III, (c)

Unit I
Introduction to image processing

A. Components of image processing system, Scenes and Images, Vector Algebra, Orthogonal Transform, Human Visual System, color vision color model: RGB, HVS, YUV, CMYK, and some basic relationships between pixels, linear and non linear operations.
B. Application of different color models in Image processing.

Unit II
Image Formation and Digitization

A. Geometric Model, Photometric Model, Sampling, Digitization, Elements of Digital Geometry, Image Properties, Representation
B. Overview of application of Image processing.

Unit III
Image Processing

A. Basic gray level transformations, histogram processing, enhancement using logical and arithmetic operations Image Enhancement - Contrast Intensification, Smoothening, Sharpening Image Restoration -- Square Error Restoration Techniques, Singular Value

(8+1 Hrs)
Unit IV
Image transform

A. Introduction to two dimensional orthogonal and unitary transforms, properties of unitary transforms one-two dimensional discrete Fourier Transform (DFT), Wavelet transforms. Cosine, sine transforms.
B. Slant, KL, affine transforms. Applications of transforms in Image processing.

Unit V (8+1 Hrs)
Compression fundamental

A. File format (bmp, tiff, pcx, gif, jpeg.) Compression fundamentals, image compression models, error free compression: LZW coding, Bit plane Coding, Lossless Predictive Coding, lossy compression: lossy Predictive Coding, Transform, vector Quantization, image compression standard: Binary Image, Continuous Tone Still Image,
B. Elements of information theory, error free compression: VLC, JPEG compression standards Factral.

Text Books

Reference Books
CS42120:: DATA MINING AND WAREHOUSING

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Database Management Systems.

Objectives:
- Know the basics of data warehousing, dimensional design
- Understand different dimensional models and other related issues
- Understand OLAP, its operations and types
- Mapping with PEOs :- III, (e)

Unit I  (8+2 Hrs)

Introduction to DWH and OLAP


B. ETL Tools, Commercial DWH Vendors/ Tools and their Comparison, Project Failure Reasons, Data Analytics, Business Intelligence, SAS Software.

Unit II  (8+1 Hrs)

Dimensional Modeling

A. Dimensional Modeling: Dimensional Model Vs ER Model, DWH Schemas: Star, Snowflake, Fact Constellation, their Comparison, Techniques to Handle Changing Dimensions, Aggregation, Families of Fact Tables, Fact Less Fact Tables; Data Warehouse Indexing: Factors used to select an Indexing Technique, Properties of a Good Indexing Technique for DWH, Indexing Techniques: Projection Index, Bitmap Index (Pure and Encoded), Join Index and their Comparison.

B. Case Studies of Data Warehouse Applications in various Industry Segments.
Unit III  
Data Mining and Functionalities  
(8+2 Hrs)

A. Introduction: Need of Data Mining, Knowledge Discovery in Database (KDD), Architecture of Data Mining System, Data Mining on Different kind of Data, Data Mining Functionalities; Data Preprocessing: Need, Cleaning, Integration, Transformation, Reduction, Discretization, Concept Hierarchy Generation; Cluster Analysis: Categories of Clustering methods, Partitioning methods: k-Means, k-Medoids ; Prediction: Numerical Prediction, Linear, Non-Linear Regression; Outlier Analysis: Applications, Techniques.

B. Data Mining Task Primitives, Query Language, System Classification, Data Mining Issues.

Unit IV  
Classification  
(9+2 Hrs)

A. Classification: Decision Tree Classifier, Rule Based Classification, Bayesian Classification, Neural Network Classification: Back Propagation Algorithm, Lazy Learner: kNN Classifier, Case-Based Reasoning, Other: Fuzzy Set Approach, Classifier Accuracy Measures, Techniques for Evaluating Classifier Accuracy; Frequent Itemset Mining: Interesting Item Set Mining: Market Basket Analysis, APriori Algorithm, Generating Association Rules, Types of Association Rules, Correlation Analysis.

B. Support Vector Machine, Associative Classification, other Classification Techniques: Genetic Algorithm, Rough Set, Constraints Based Association Mining.

Unit V  
Data Mining on different Databases  
(7+1 Hrs)

A. Multimedia Data Mining, Web Mining, Text Mining, Spatial Data Mining, Mining on Social Networks, Multirelational Data Mining.

B. Data Mining Applications, Trends/ Challenges of Data Mining, Mining Sequence Patterns in Transactional Database, Graph Mining, Data Mining Tools- Dbminer/ WEKA/ Oracle DM Tools/ OLE DB/ Ida.

Text Books


Structure & Syllabus of  B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
Reference Books

Additional Readings
CS42112: DISTRIBUTED SYSTEMS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Operating Systems.

Objectives:
To study the different design issues of Distributed Operating Systems.
- Create an awareness of the major technical challenges in distributed systems design and implementation.
- Expose students to current technology and distributed infrastructure software.
- Expose students to past and current research issues in the field of distributed systems.
- Provide experience in the implementation of typical algorithms used in distributed systems.
- Mapping with PEOs :- II, III, (c)

Unit I  (8+1 Hrs)
Introduction

A. Introduction to Distributed Systems, Examples of Distributed Systems, Hardware and Software Concepts, Design issues.
B. Distributed Systems Goals, Distributed Computing System models.

Unit II  (8+1 Hrs)
Communication

A. Interprocess Communication: Message Oriented Communication, Stream Oriented Communication.
B. LRPC, Events and Notifications.

Unit III  (8+1 Hrs)
Synchronization
A. Time and Global States: Clock Synchronization, Logical Clocks, Global State, Event Ordering.
Distributed Deadlock Algorithms for Avoidance, Prevention, and Detection: Centralized Approach, Hierarchical Approach, WFG Based Fully Distributed, Deadlock Recovery.
Distributed Transaction: Transaction Model, Classification, Implementation, Concurrency Control: 2 Phase Locking, Strict 2 PL.

Unit IV (8+1 Hrs)
Fault Tolerance
A. Introduction to Fault Tolerance, Failure Models, Failure Masking by Redundancy: Triple Modular Redundancy.
Reliable Client Server Communication, Reliable Group Communication.
Distributed Commit: 2 Phase Commit.
B. Distributed Commit: 3 Phase Commit and Comparison of 2PC and 3PC, Distributed Recovery.

Unit V (8+1 Hrs)
Distributed File Systems and Distributed Shared Memory
A. Name Services: Name Entities, Name Resolution: Linking, Mounting, Iterative and Recursive Algorithms, Case Study: DNS Directory, X.500 Distributed Naming Service
Distributed File Systems: SUN NFS, CODA.
Distributed Shared Memory: Introduction, Architecture of DSM Systems
Design and Implementation Issues of DSM: Granularity, Structure of Shared Memory Space, Consistency Models, Replacement Strategies, Thrashing, Heterogeneous DSM.
Case Studies: Mach, Amoeba.
B. Distributed File Systems: SFS, xFS, Plan 9, Comparison of different DSF, Distributed Shared Memory: Grid, Clusters.

Text Books
Reference Books

Additional Reading
CS42117:: ARTIFICIAL INTELLIGENCE

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To provide a strong foundation of fundamental concepts in Artificial Intelligence
- To provide a basic exposition to the goals and methods of Artificial Intelligence
- To enable the student to apply these techniques in applications which involve perception, reasoning and learning.
- To learn and understand the knowledge representation techniques for knowledge base
- Mapping with PEOs :- III,IV, (f)

Unit I  (8+1 Hrs)

Introduction
B. Criteria for Success, Turing Test.

Unit II  (8+1 Hrs)

Heuristics Search Techniques
B. Applications Minimax Algorithm.
Unit III  
**Predicate Logic**  
(8+1 Hrs)  
A. Using predicate logic: Predicate Calculus, Predicate and arguments, ISA Hierarchy, Frame notation, Resolution, Natural Deduction.  
Representing simple facts in Logic - Logic Programming, Computable functions in predicates, resolution, unification, Forward and backward reasoning, Expert System, Basics of PROLOG.  
B. Propositional Logic, Case study of Expert system in PROLOG.  

Unit IV  
**Structured Knowledge Representation**  
(8+1 Hrs)  
A. TMS (Truth maintenance system), Statistical and probabilistic reasoning, Associative Networks, Semantic Nets, Frames Structures, Learning – Concept of Learning – Learning Automata, Genetic Algorithm, Learning by induction  
Planning: Block world, strips, Implementation using goal stack, Non linear planning, Hierarchical planning, and least commitment strategy.  
B. Conceptual Dependency & Scripts, Various types of learning.  

Unit V  
**Natural Language Processing**  
(8+1 Hrs)  
B. Augmented Transition Network, Understanding.  

Unit VI  
Consist of part B of Unit I to Unit V  

Text Books  

Reference Books  
2. "Introduction to Artificial Intelligence.", Eugene, Charniak, Drew Mcdermott, Addison Wesley  

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’, Issue 03, Rev01 dated 02/04/2011
Additional Reading

1. “Introduction to AI and Expert Systems”, Patterson, PHI
3. “Artificial Intelligence and Intelligent Systems”, N.P. Padhy, OXFORD, 2005
CS42119:: INFORMATION RETRIEVAL

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Data Communication.

Objectives:
• To understand some of the common data link layer protocols.
• To learn various routing algorithms.
• To understand various protocols used at different layers.
• Mapping with PEOs :- III, (a)

Unit I
Introduction

(9 Hrs)


B. Models for Browsing
   SLIP, SONET, MPLS.

Unit II
Retrieval Evaluation and Querying

(8 Hrs)

A. Recall and Precision, Alternative Measures, Reference Collections, Keyword Based Querying, Pattern Matching, Structural Queries, User Relevance Feedback.

B. Automatic Local Analysis, Automatic Global Analysis.

Unit III
Indexing and Searching

(8 Hrs)

A. Inverted Files, Suffix Trees and Suffix Arrays, Signature Files, Boolean Queries, Sequential Searching, Pattern Matching, String Matching Allowing Errors, Regular Expressions and Extended Patterns, Pattern Matching using Indices, Structural Queries.

B. Compression.

Unit IV
Web Searching

(8 Hrs)

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
A. Characterizing the Web, Search Engines, Browsing, Metasearchers, Searching using Hyperlinks, Parallel IR, Distributed IR,
B. Web Issues.

Unit V (7 Hrs)
Multimedia IR

A. Data Modeling, Query Languages, A Generic Multimedia Indexing Approach, One Dimensional Time Series, Two Dimensional Color Images, Automatic Feature
B. Image features and similarity functions.

Text Books

Reference Books
CS42121:: COMPUTER ARCHITECTURE

**Credits**: 03  
**Teaching Scheme**: - Theory 3 Hrs/Week

**Prerequisites**: Computer Organization, Operating Systems, Microprocessors

**Objectives**: 
- To understand the concept of Parallel Processing and Parallel Architectures
- Mapping with PEOs: - I,II, (b)

**Unit I**  
Overview of Parallel Processing


B. Applications of Parallel Processing

**Unit II**  
Pipeline Architecture

A. Principles and implementation of Pipelining, Classification of pipelining processors, Arithmetic and Instruction pipelining, Instruction level parallelism (ILP) Pipelining hazards and resolving techniques, Data forwarding, register renaming pipeline reservation table, sequencing and collision, Branch problem, branch penalty .prediction. Delayed branching Branch target buffer Advanced pipelining techniques, loop unrolling techniques, br Superscalar pipelining, speedup, in order, out of order execution, VLIW processor software scheduling, Software pipelining.

B. EPIC IA 64, Predicated execution, speculative loading, Register stack engine, Case study: Super scalar Architecture- Pentium

**Unit III**  
Vector and Array Processor

Register and Memory to Memory Architectures Vector performance, Vector Chaining, Vector loops and recurrence vectorizers and optimizers, Cray I Cray Y-MP

SIMD Computer Organization: Distributed & shared memory model Processing Element (PE) Masking and Data Routing Inter PE Communication Interconnection networks of SIMD, Static Vs Dynamic network, Permutations, Shuffle exchange, Bit reversal hyper cube, mesh network, Iliac, omega, blocking, non blocking networks.

Compute Unified Device Architecture (CUDA) Hardware, Memory, Thread model Parallel Algorithms For Array Processors; Matrix Multiplication. Sorting on mesh, hypercube and CUDA Vectorization and Parallelization methods

B. Systolic Architecture ,CM-2,Cyber 205, Illiac Architecture

Unit IV (8+1 Hrs)

Multiprocessor Architecture

A. UMA, NUMA, COMA MPP Processor. Loosely and Tightly coupled multiprocessors, characteristics of multiprocessors & multiprocessing, Inter Processor communication network, Time shared bus, Crossbar switch, Interleaved memories S access, C access.
Cache coherency and bus snooping and directory based protocols. Massively Parallel Processors (MPP), Inter Processor Communication and Synchronization.

Software multithreading, thread concept, lightweight process (LWP), bounded, unbounded threads, thread management : thread scheduling & control by thread library & kernel (State transition diagram)

B. Cluster and grid computing concepts.

Unit V (8+1 Hrs)

Parallel Software Issues

A. Operating systems for multiprocessors systems, software issues for multiprocessors
Message Passing Interface (MPI) Principles of Parallel Algorithm design: Decomposition, tasks and Dependency graphs. Parallel language constructs. Programming using message passing and shared memory paradigms (Sorting, searching, matrix problems)
Parallel programming languages POSIX thread, Java threads

B. Writing parallel programs, analytical modeling of parallel programs
Text Books

Reference Books:
CS42123:: BUSINESS INTELLIGENCE

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Database Management Systems.

Objectives:
- To understand the technology and processes associated with Business Intelligence framework.
- To demonstrate understanding of Data Warehouse implementation methodology and project life cycle.
- To understand how to identify the metrics, indicators and make recommendations to achieve the business goal for a business scenario.
- Mapping with PEOs: IV, V, (h), (i).

Unit I (8+1 Hrs)
Introduction to Business Intelligence

Data Warehouse versus Data Marts, Data Warehouse Architecture. Introduction to SAS.

B. Illustrating the dependability and integration of ERP, SCM and E-commerce with BI.
Commercial BI vendors / and their comparison

Unit II (8+1 Hrs)
Introduction to Multi-Dimensional Data Modeling

A. Introduction to Data and Dimension Modeling, Multidimensional Data Model, ER Modeling vs. Multi Dimensional Modeling, Concepts of Dimensions, Facts, Cubes, Attribute, Hierarchies, Star and Snowflake Schema, Introduction to Business Metrics and KPIs, Creating Cubes using SSAS.
OLAP operations

B. Techniques to handle changing dimensions, families of fact tables, fact less fact table.

Unit III (8+1 Hrs)
Data Cube Computation

A. OLAP Query processing, Indexing techniques bitmap, join index and their comparison
Efficient methods for Data cube computation: cube materialization, multiway array aggregation, BUC, Star cubing.
Further development of data cube and OLAP technology
B. Attribute oriented induction for data generalization and concept description.

Unit IV
Basics of Data Integration (Extraction Transformation Loading) (8+1 Hrs)
A. Concepts of data integration need and advantages of using data integration, introduction to common data integration approaches, introduction to ETL using SSIS, Introduction to data quality, data profiling concepts and applications.
B. Data Cleaning, Reduction and Transformation operators. Commercial ETL tools.

Unit V
Basics of Enterprise Reporting (8+1 Hrs)
B. Types of Enterprise Reports, Components of Reporting System.

Text Books

Reference Books
CS40201:: HUMAN COMPUTER INTERACTION

Credits: 01

Teaching Scheme: - 1 Hr/Week

Prerequisites: : Nil

Objectives:
- To identify and incorporate key social, cognitive and physiological factors that influence user perceptions, understandings and usages of Information Technology.
- To motivate students for interviews and field-work with users for improved practical approach in interface/interaction design.
- Mapping with PEOs :- II. III. (c)

List of Contents

A TERM-WORK containing the record of the following:

1. Design user persona for the users of selected product/system.
2. Perform GOMS analysis for selected product/system.
3. Conduct a contextual inquiry for selected product/system.
4. Design an interface prototype for selected product/system.
5. Evaluate an interface using usability testing / evaluation technique.

Text Books


Reference Books

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’, Issue 03, Rev01 dated 02/04/2011


Additional Reading


CS42220:: DATA MINING AND WAREHOUSING

Credits: 01  
Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: Database Management Systems.

Objectives:
- Study different functionalities of data mining, various algorithms, and get an overview of significant data mining applications
- Mapping with PEOs :- III, (e)

List of Contents

A TERM-WORK containing the record of the following:

1. Design a star / snowflake schema for a data warehouse for an organization.
2. Use ETL tool for extract-transform-load operations
3. OLAP Data cube implementation and operations
4. Data mining: Implement clustering algorithms
5. Classification algorithms
6. Association rule mining
7. Prediction, Linear Regression
8. Outlier analysis

Text Books

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
Reference Books

Additional Readings
CS42212:: DISTRIBUTED SYSTEMS

Credits: 01  
Teaching Scheme: -Tutorial 1 Hr/Week

Prerequisites: Operating Systems.

Objectives:
- To study the different design issues of Distributed Operating Systems.
- To study various methods of communication in Distributed Operating Systems.
- To analyze Distributed File System.
- Mapping with PEOs :- II, III, (c)

List of Contents

A TERM-WORK containing the record of the following:

1. Implement Remote Method Invocation.
2. Simulate Cristian’s or Berkeley’s algorithm for clock synchronization.
3. Simulate Lamport’s algorithm for clock synchronization using counters or physical clock.
4. Simulate the Ring or Bully election algorithm.
5. Simulate the Ricart Agrawala’s distributed algorithm for mutual exclusion.
7. Simulate Wait for Graph based Centralized or Hierarchical or Distributed algorithm for deadlock detection.
8. Simulate Recursive or Iterative name resolution algorithm.
9. Simulate the Byzantine General’s Problem.
10. Simulate any one of the Consistency models.
Text Books

Reference Books

Additional Reading:
CS42217 :: ARTIFICIAL INTELLIGENCE

Credits: 01

Teaching Scheme: - - 1 Hr/Week

Prerequisites: : Nil

Objectives:
- To provide a strong foundation of fundamental concepts in Artificial Intelligence
- To provide a basic exposition to the goals and methods of Artificial Intelligence
- To enable the student to apply these techniques in applications which involve perception, reasoning and learning.
- To learn and understand the knowledge representation techniques for knowledge base.
- Mapping with PEOs :- III, IV, (f)

List of Contents

A TERM-WORK containing the record of the following:

Assignments :
- Implement Tic-tack-toe problem using heuristic functions.
- Implement A* Algorithm with any example
- Implement AO* algorithm
- Implement mini-max search to solve at least two game problems
- Implement the concept of constraint satisfaction problem
- Implement the concept of Resolution in knowledge Representation.
- Implement Goal stack planning
- Implementation of truth maintenance system using prolog
- Development of mini-expert system using prolog
Text Books

Reference Books
2. "Introduction to Artificial Intelligence.", Eugene, Charniak, Drew Mcdermott, Addison Wesley

Additional Reading
1. “Introduction to AI and Expert Systems”, Patterson,PHI
3. “ Artificial Intelligence and Intelligent Systems”, N.P. Padhy, OXFORD, 2005
CS42219:: INFORMATION RETRIEVAL

Credits: 01

Teaching Scheme: - Tutorial 1 Hrs/Week

Prerequisites: Data Communication.

Objectives:
- To understand some of the common data link layer protocols.
- To learn various routing algorithms.
- To understand various protocols used at different layers.
- Mapping with PEOs :- III, (e)

List of Contents

A TERM-WORK containing the record of the following:

A. Assignments :

1. Write a program for text retrieval using fuzzy set model.
2. Write a program for text retrieval using probabilistic model.
3. Write a program for keyword based text retrieval.
4. Write a program for text retrieval using pattern matching.
5. Write a program for creating inverted index file.
7. Study of search engine architecture.

Text Books

Reference Books

CS42221:: COMPUTER ARCHITECTURE

Credits: 01 Teaching Scheme: - Tutorial 1 Hr/Week

Objectives:
1. To build analytical thinking
2. To understand the practical implementation of parallelism with problem solving.
3. To study Compute Unified Device Architecture and implement parallel algorithms
4. Mapping with PEOs :- I, II, (b)

Course Instructor will assign problems relevant to Units I to V. Students will implement the assignments in laboratory. Group of 2-3 students will be given different problems and will have to present it for assessment

1. Problems from Chapters 1, 2, 3 (Text book 1.)
2. Problems from Chapters 6, 7, 8 (Text book 1.)
3. Simulate a SIMD Machine with programmable PEs and solve Sorting problems.
4. Implement prefix, dot product computation in CUDA
5. Matrix operations in CUDA and simulation on Hypercube.
6. Case Studies of current supercomputers.
CS42223:: BUSINESS INTELLIGENCE

Credits: 01  
Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: Database Management Systems.

Objectives:
- To understand the technology and processes associated with Business Intelligence framework.
- To demonstrate understanding of Data Warehouse implementation methodology and project life cycle.
- To understand how to identify the metrics, indicators and make recommendations to achieve the business goal for a business scenario.
- Mapping with PEOs: IV, V, (h), (i).

List of Contents

A TERM-WORK containing the record of the following:

Assignments:

1. Design a multi dimensional data model using star/ snowflake schema technique for any business fact corresponding to a department of an organization.
2. Implement the designed schema and create cubes using SSAS.
3. Create two distinct data sources (e.g. text file, relational database and Excel, etc) and apply ETL process using SSIS to load data in the created cube.
4. Use SSRS functionalities to generate reports analyzing the designed cube.

Text Books
Reference Books
CS41305 :: DATA ACQUISITION SYSTEMS

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites:

Objectives:
- To study programming model of MCS-51 and
- I/O interfacing of sensors with MCS-51.
- Mapping with PEOs: - II, III, (c)

List of Practical

1. Introduction of MCS-51 training kit
2. Interfacing of I/O devices with MCS-51
   a. switches
   b. LED’s etc.
3. Write MCS-51 program to use timer in interrupt mode.
4. OpAmp applications I
   a. i OpAmp as amplifier (inverting and non inverting)
   b. ii. OpAmp as adder and subtractor
5. OpAmp applications II
6. Integrator and differentiator
7. High pass filter and Low pass filter
8. Interface LCD with MCS-51.
9. Interface sensors (thermal, optocoupler etc.) with MCS-51.
10. Interface sensors (magnetic ) with MCS-51.
11. Interface R_2R ladder with MCS-51.
12. Assignment based on PLC.
Text Books


Reference Books

CS42304:: NEURAL NETWORKS

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites:

Objectives:
- To give design methodologies for artificial neural networks.
- To demonstrate neural network applications on real-world tasks.
- Mapping with PEOs :- III, IV, (f)

List of Practical

1. Implement any classification problem using Single Layer Perceptron.
2. Design a Perceptron Network for handwritten digit recognition.
3. Design a Backpropogation Network for any real time application.
4. Design a Kohonen network for any real time application.
5. Implement the concept of Associative memory using Hopfield Network.
6. Implement the concept of ART.
7. A Mini Project using ANN.

Text Books

Reference Books
CS42303:: PARALLEL COMPUTING ON GPU

Credits: 01  Teaching Scheme: - Laboratory  2 Hrs/Week

Prerequisites: Nil

Objectives:
1. Students will be able to detect parallelism in a given problem
2. Students can design parallel algorithm for a given problem for GPU
3. Students will be able to write parallel programs to execute on GPU

List of Practical

1. Parallel GPU implementation of vector-vector operations
2. Parallel GPU implementation of vector-Matrix operations
3. Parallel computation of binomial coefficient matrix
4. Parallel GPU implementation of Matrix-Matrix operations
5. Assignment focusing on optimization of data transfer between CPU and GPU: using page locked host memory and to avoid the data transfer
6. Assignment focusing on memory optimization: use of GPU shared, constant and texture memory.
7. Parallel GPU implementation involving kernel looping.
8. Parallel computation of set of multi-indices on GPU.

Text Books
6. CUDA by Example: An Introduction to General-Purpose GPU Programming by Jason Sanders and Edward Kandrot
7. Parallel Programming in C with MPI and OpenMP by Michael J. Quinn, Tata McGraw-Hill Edition
References:

2. www tutorials on introduction to parallel computing
3. Other references suggested by instructor
CS42305 :: MOBILE COMPUTING

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Computer Networks

Objectives:
- Understand the basic of mobile communication.
- Understand the gaming applications on mobile.
- Understand the transfer of files in mobile network.
- Mapping with PEOs: I.V, (j)

List of Practical

Assignments should be implemented on android operating systems.

1. Design simple GUI application with activity and intents e.g. calculator.
2. Design an application to handle address book and call log.
3. Design an application to display map with current location.
4. Design an application to send SMS and emails.
5. Design a client server application to communicate with database server.
6. Design an application for voice calling over IP.
7. Design an application for sending files.
8. Design a gaming application.
9. Design GSM network of 5 nodes and implement handoffs.
10. Design a GSM network and access the channel using ALOHA.

Text Books

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
Reference Books

CS42302:: IMAGE PROCESSING

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Nil

List of Practical

All the assignments should be done using ‘MATLAB ’.
Optional support of 'C ‘may be used.

1. Study of different file formats e.g. BMP, TIFF and extraction of attributes of BMP.

2. Study of statistical properties- mean, standard deviation, profile, variance and Histogram Plotting.

3. Histogram equalization & modification.

4. Gray level transformations such as contrast stretching, negative, power law transformation etc.

5. Spatial Domain filtering- smoothing & sharpening filters.

6. DCT/IDCT of given image.

7. Edge detection using Sobel, Prewitt and Roberts operators.


10. Creating noisy image and filtering using MATLAB.
Text Books

Reference Books
CS47303 :: PROJECT STAGE 2

Credits: 04
Teaching Scheme: - 6 Hrs/Week

Aim
This course addresses the issues associated with the successful management of a software development project. The course emphasizes project life cycle phases requirement engineering, system analysis and system design. A further aim is for students to heighten personal awareness of the importance of developing strategies for themselves and their career. The Project Work can lead to:

a. Transform existing systems into conceptual models.
b. Transform conceptual models into determinable models.
c. Use determinable models to obtain system specifications.
d. Select optimum specifications and create physical models.
e. Apply the results from physical models to create real target systems.

Objectives
1. To provide an understanding of the project planning process and problem scoping
2. To define roles and work with cross functional teams
3. To establish clear project objectives and milestones
4. To create effective and deliverable project plans
5. To perform rigorous analysis and design activities for solution planning
6. To select appropriate technology for implementation and testing
7. Mapping with PEOs :- – III, IV, (f)

Overview of the Course:

1. The Student Project Group is expected to make a survey of situation for identifying the requirements of selected Technological Problem. The Student Project Group will be monitored by Internal Guides and External Guides (if any).
2. The project requires the students to conceive, design, implement and operate a mechanism (the design problem). The mechanism may be entirely of the student’s own design, or it may incorporate off-the-shelf parts. If the mechanism incorporates off-the-shelf parts, the students must perform appropriate analysis to show that the parts are suitable for their intended purpose in the mechanism.
3. The project must be open-ended – meaning that there is not a known correct answer to the design problem. Students are expected to apply their creativity (simply copying or re-creating something that already exists is not acceptable).
4. The project must have an experimental component. Students must conceive, design, implement and operate an appropriate experiment as part of the project.
The experiment might be to collect data about some aspect of the design (i.e., to verify that the design will work as expected). Alternatively, the experiment could be to verify that the final mechanism performs as expected.

5. Upon receiving the approval, the Student Project Group will prepare a preliminary project report consisting Requirement Definition Document, Feasibility Study Document, System Requirement Specification, System Analysis Document, Preliminary System Design Document. All the documents indicated will have a prescribed format.

6. The Project Work will be assessed jointly by a panel of examiners having more than Five Years experience. The Project Groups will deliver the presentation of the Project Work which will be assessed by the panel.

7. The Student Project Group needs to actively participate in the presentation. The panel of examiners will evaluate the candidate’s performance based on presentation skills, questions based on the Project Work, understanding of the Project, analysis and design performed for the project.

8. The Student Project Groups are expected to work on the recommendations given by the panel of examiners. In no case any variation in Project Theme will be permitted.

**Assessment Scheme**

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<thead>
<tr>
<th>Sr. No.</th>
<th>Content</th>
<th>Marks</th>
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<td>1</td>
<td>System Requirement Specification</td>
<td>20</td>
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<td>2</td>
<td>Feasibility Study</td>
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<td>3</td>
<td>System Analysis</td>
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<td>4</td>
<td>System Design</td>
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<tr>
<td>5</td>
<td>Presentation of the Project Work</td>
<td>10</td>
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</tbody>
</table>

**Outcomes:**

Upon completion of the course, the students will be to:

1. Identify major issues in complex situations; and know how to prepare alternative solutions and make decisions.
2. Becoming aware of the need to think and act in an entrepreneurial manner by developing the ability to: critically and creatively understand innovations and development, work independently and collaboratively.
Note:
The student needs to identify a technological problem in the area of Computer Engineering or Information Technology of their choice and address the problem by formulating a solution for the identified problem. The project work needs to be undertaken by a group of maximum FOUR and minimum of THREE students. The Project work will be jointly performed by the project team members. The Project Group will prepare a synopsis of the project work which will be approved by the concerned faculty member. The project should not be a reengineering or reverse engineering project. In some cases, reverse engineering projects will be permissible based on the research component involved in it. The project work aims at solving a real world technical problem. Hence ample literature survey is required to be done by the students. Application-oriented projects will not be acceptable. Low-level custom User Interface development and its allied mapping with a particular technology will not be accepted. Following is the list of recommended domains for Project Work:

<table>
<thead>
<tr>
<th>Computer Networks</th>
<th>Image Processing</th>
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<tbody>
<tr>
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<td>Artificial intelligence</td>
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<td>Network Security</td>
<td>Expert Systems</td>
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<td>Digital Signal Processing</td>
<td>Object Oriented Systems</td>
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<td>Systems Programming</td>
<td>Modeling and Design</td>
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<td>Real Time Systems</td>
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<td>Embedded systems</td>
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<td>Cluster Computing</td>
<td>Client-Server Computing</td>
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<td>Mobile &amp;Wireless Communications</td>
<td>Cloud Computing</td>
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<td>Multimedia Systems</td>
<td>Protocol Engineering</td>
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MODULE VIII
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<th>Subject No.</th>
<th>Subject Code</th>
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<th>Teaching Scheme (Hrs/week)</th>
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<td>S6</td>
<td>CS40104</td>
<td>Software Testing &amp; Quality Assurance</td>
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<td>S6</td>
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<td>Network Security</td>
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CS 40104:: SOFTWARE TESTING & QUALITY ASSURANCE

Credits: 03  Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Software Engineering

Objectives:
• To understand Software Measurement Theory and Software Test Automation
• To practice Software Testing Techniques and Strategies
• Understand the fundamental concepts, approaches, and methodologies in software quality management and assurance.
• Understand the framework and general approach of several Quality System Standards and Total Quality Management (TQM).
• Mapping with PEOs :- III ,(e).

Unit I  (7+1 Hrs)
Software Measurement


B. SRE process, Reliability Concepts: Hardware and Software, Deploying SRE

Unit II  (9+1 Hrs)
Principles of Testing


Black-Box Testing: Test Case Design Criteria, Requirement Based Testing, Positive and
Negative Testing, Boundary Value Analysis, Equivalence Partitioning State Based Testing, Domain Testing
B. Analysis of Flow Graphs, Complexity Measures and computations

Unit III

Functional Testing


Unit IV

Higher Order Testing


Unit V

Software Quality Assurance

B. Software Benchmarks and Baselines, Key Factors for Software Assessment and Benchmark Studies, Identifying Software Best and Worst Practices
Text Books


Reference Books


Additional Reading

CS40108 :: NETWORK SECURITY

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Computer Networks.

Objectives:
- To understand various attacks in the network.
- To learn various cryptographic techniques.
- To understand authentication techniques.
- Mapping with PEOs :- IV,V,(i).

Unit I  
Introduction

Software vulnerabilities: Phishing, buffer overflow, formatted string attack, Cross-site scripting attack. Virus and Worm Features, Internet Scanning Worms.
B. Protocol analyzer (Wireshark), Spoofing tool.

Unit II  
Private key cryptography

A. Mathematical background for cryptography: modulo arithmetic, GCD (Euclids algorithm), algebraic structures (Groups, Rings, Fields, Polynomial Field). Data Encryption Standard: Fiestel structure, round function, modes of operation. Message authentication code (MAC), Attacks on DES, linear cryptanalysis.
B. Chinese remainder theorem, Elementary Ciphers (Substitution, Transposition and their Properties).

Unit III  
Public key cryptography

A. RSA: Key generation in RSA, mathematics behind RSA, practical issues in RSA, attacks on RSA, Public Key Cryptography Standards (PKCS), Key management (digital certificate, public key infrastructure).
Cryptographic hash: properties, applications (hashed based MAC, digital signature), performance, attacks.
Diffie-Hellman key exchange: protocol, parameter choice, attack.
Structure & Syllabus of  B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
Elliptic Curve Cryptography (ECC): equation of a curve, find the inverse, ECC over prime fields, ECC over binary field.

B. Diffie-Hellman key exchange on EC Groups, EC-based digital signature.

Unit IV  
**Authentication and access control**  


Access Control in Operating Systems: Discretionary Access Control, Mandatory Access Control, Role Based Access Control.

B. Recent trends in Operating system.

Unit V  
**Applying security**  


Transport layer security: SSL and TLS.

Application layer security: Security services, web security considerations, and S/MIME, PGP, PEM, HTTPS, Honey pots, VPN.

B. Cyber laws.

**Text Books**


**Reference Books**


**Additional Reading**


CS40108 :: NETWORK SECURITY

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Computer Networks.

Objectives:

- To understand various attacks in the network.
- To learn various cryptographic techniques.
- To understand authentication techniques.
- Mapping with PEOs :- IV,V,(i).

Unit I
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Structure & Syllabus of B.E.(Information Technology) Program-Pattern ‘E11’, Issue 03, Rev01 dated 02/04/2011
Elliptic Curve Cryptography (ECC): equation of a curve, find the inverse, ECC over prime fields, ECC over binary field.
B. Diffie-Hellman key exchange on EC Groups, EC-based digital signature.

**Unit IV**  
(8+1 Hrs)  
**Authentication and access control**

Access Control in Operating Systems: Discretionary Access Control, Mandatory Access Control, Role Based Access Control.
B. Recent trends in Operating system.

**Unit V**  
(8+1 Hrs)  
**Applying security**

A. Network layer security: IPSec for IPV4 and IPV6.
Transport layer security: SSL and TLS.
Application layer security: Security services, web security considerations, and S/MIME, PGP, PEM, Https, Honey pots, VPN.
B. Cyber laws.

**Text Books**


**Reference Books**


**Additional Reading**

CS 42107:: SOFTWARE DESIGN AND ARCHITECTURE

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Software Engineering

Objectives:
- To describe what patterns are, how to use them, and why they are important.
- To understand contribution of patterns towards the design process efficiency
- To trace the relationship between patterns and traditional methods
- To learn the relationship among patterns, frameworks, object-oriented languages, and software architecture
- To identify fundamental and advanced concepts of design and architectural patterns
- To Structure systems by applying architectural patterns
- To focus on Archetypes and Architecture Documentation

Unit I  
Introduction to Software Architecture and Design (8+1 Hrs)


B. CASE study of Architectures: Air traffic control case study, Flight simulation case study, SOA, SOMF, TOGAF, CORBA, EJB.

Unit II  
Architectural Patterns and Design Patterns (8+1 Hrs)
A. Introduction to software Patterns: Architectural Patterns, Design Patterns and Idioms. Architectural Patterns: Blackboard, Pipe and filter, Design Patterns proposed by GoF: Creational Patterns, Structural Patterns, and Behavioral Patterns. Design Patterns proposed by Buchman: Object Management Patterns Adaptation Patterns, Communication Patterns, Patterns for Distribution, Patterns for Interactive Systems, Adaptable Systems, Analysis Patterns.
B. Design patterns in java, software design anti-patterns.

Unit III (8+1 Hrs)
Enterprise Architecture Patterns

A. Layering, Organizing Domain Logic, Mapping to Relational Databases, Web Presentation, Domain Logic Patterns, Data Source Architectural Patterns, Object-Relational Behavioral Patterns, Object-Relational Structural Patterns, Object-Relational Metadata Mapping Patterns, Web Presentation Patterns, Distribution Patterns, Offline Concurrency Patterns
B. TADG Architecture Patterns, IBM Patterns for e-business

Unit IV (8+1 Hrs)
Enterprise Architecture Integration

A. Defining EAI, Data-Level EAI, Application Interface-Level EAI, Method-Level EAI, User Interface-Level EAI, The EAI Process, An Introduction to EAI and Middleware, Transactional Middleware and EAI, RPCs, Messaging, and EAI, Distributed Objects and EAI, Database-Oriented Middleware and EAI, Java Middleware and EAI, Implementing and Integrating Packaged Applications, XML and EAI, Message Brokers, Process Automation and EAI.
B. Enterprise Integration Patterns

Unit V (8+1 Hrs)
Archetype Patterns

A. Archetypes and Archetype Patterns, Model Driven Architecture with Archetype Patterns. Literate Modeling, Archetype Pattern, Customer Relationship Management (CRM) Archetype Pattern, Product Archetype Pattern, Quantity Archetype Pattern, Rule Archetype Pattern
B. Application of archetypes in a particular domain to understand pattern mapping.

Text Books

Reference Books

Additional Reading
CS42101:: ADVANCED COMPUTER GRAPHICS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Computer Graphics

Objectives:
- To learn about recent research advances in Computer Graphics, Computational Geometry, Interactive Techniques, and Visualization.
- To gain first-hand experience with the challenges of efficient and accurate modeling, rendering, and simulation, and the necessary data structures and algorithms.
- To explain the principles and techniques underlying 3D computer graphics and 3D API.
- To develop programming skills in 3D computer graphics.
- To introduce advanced techniques for 3D rendering and modeling.
- Implement key components of the rendering pipeline, especially visibility, rasterization, viewing, and shading.
- Mapping with PEOs: V, (j).

Unit I  
OpenGL  
(8+1 Hrs)

A. OpenGL over windows, SDK, Extensions, GLUT, GLU, OpenGL primitives, Programming language: Blending, 3D viewing(camera analogy), Lighting model, Culling, Fog, Texture mapping.

B. OpenGL over Linux, pBuffer rendering, Shadowing Techniques.

Unit II  
Graphics Hardware  
(8+1 Hrs)

A. Graphics card, Components of graphics card, General-Purpose computation, GPU, GPGPU, CUDA architecture, CUDA Programming: Type qualifiers, Execution configuration, vector types, functions, APIs.

B. How graphics card works? GeForce 6800 series GPU architecture.
Unit III (8+1 Hrs)
Advanced Rendering Techniques

A. Point based rendering, Mesh Simplification, Spatial partitioning, Solid Modeling, Subdivision surfaces: Catmull-Clark subdivision, Subdivision rules, Visibility Computation: culling types, cells and portals, hardware support.
B. Splines, Tessellation, 3D viewing.

Unit IV (8+1 Hrs)
Photorealistic and Volume Rendering

B. Monte Carlo mathematical formulation, Marching cubes algorithm.

Unit V (8+1 Hrs)
Texture Synthesis and Image Processing

A. Texture synthesis, Image processing: Digital image representation, Image data structures, Sampling and Quantization, Image enhancement in spatial domain.
B. Image compression, Image synthesis.

Text Books

Reference Books

Additional Reading
CS42108: SYSTEMS PROGRAMMING

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites:
1. Data Structures
2. Computer Organization
3. C programming

Objectives:
1. To understand the concepts and components of Systems Programming
2. To Learn and understand the fundamentals of Operating systems.
3. Mapping with PEOs :- II, III, (d)

Unit I
Introduction to System Programming

A. System Software: Concept, introduction to various system programs such as Assemblers, Assembler design, Assembler design options, Loaders, Linkers, Macro Processors, Compilers, Interpreters, Operating Systems, Introduction to Device Drivers. System software machine architecture, the simplified instructional computer, Traditional machines - RISC machines. Machine dependent and machine independent Assembler features, Implementation.
B. Examples - AIX Assembler, Boot strap loaders and implementation examples- MS-DOS Linker, Sun OS linker

Unit II
Machine & Assembly Languages

B. Instruction description, Pseudo operations, Instruction Mapping

Unit III
Encoding and Decoding
A. Encoding and decoding schemes for the X-86 processor, Covering the entire Instruction set and all Memory Addressing Formats. System Programming Examples: Operating system interfaces, Stack smashing.
B. Library Description for IA-32/Intel64.

Unit IV (8+1 Hrs)
Introduction to Linux
A. Introduction and essential concepts of LINUX system programming: System Programming, APIs and ABIs, standards, concepts of Linux programming. Program segments/sections; The ELF Format; Linking and loading; Linux dynamic libraries (shared objects); Multitasking and paging; Address translation; Memory Protection; Comparison with Windows.
B. Dynamic linking, API compatibility, Dynamically linked libraries, Overall architecture and limitations.

Unit V (8+1 Hrs)
DirectX Programming
A. Types of Drivers, Driver History, Driver Issues, Writing a Driver, Device Driver Stack Buses and Physical Devices, PnP, Device Namespace, and Named Devices.
Direct X: History, Components, Compatibility, Architecture.
B. .Net Framework and DirectX

Text Books

Reference Books
CS42110:: MULTIMEDIA SYSTEMS

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To introduce students to the different media used and the design issues in multimedia systems with understanding of multimedia programming.
- To train students in the application of suitable evaluation techniques for multimedia systems.
- Provide an opportunity for students to apply design, implementation and evaluation concepts and techniques to the development of a realistic multimedia system.
- Mapping with PEOs :- II, III, (c)

Unit I  (8+1 Hrs)
Introduction to multimedia

A. What is multimedia? characteristics of multimedia presentation, Multimedia building blocks, multimedia and internet, Multimedia architecture, Windows multimedia support, Hardware support, Distributed multimedia applications, Streaming technologies, Multimedia database systems, Multimedia authoring tools, overview of multimedia software tools,
Input-Output technologies: Key technology issue, Pen input, Video and image display system, Printout technology, Image scanner, Digital voice and audio, Full motion video.
B. Multimedia Document Architecture, (MHEG, SGML, ODA, OMF etc.), Multimedia applications.

Unit II  (8+1 Hrs)
Text and Image

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
B. Hybrid: JPEG-DCT, MMDBS-Feature extraction of images.

Unit III (8+1 Hrs)

Audio

A. Nature of sound waves, characteristics of sound waves, digital representation of audio, psychoacoustic, and elements of audio systems: Microphone, amplifiers, speakers, synthesizer, sound card, MIDI, digital audio, CD formats. Audio file formats: WAV, AIFF, VOC, AVI, MPEG Audio File formats, RMF, WMA etc.

B. MMDBS – Feature extraction of Audio.

Unit IV (8+1 Hrs)

Video


B. MMDBS – Feature extraction of Video.

Unit V (8+1 Hrs)

Multimedia Application Design

A. Virtual reality design, Application workflow design issues, Distributed application design issues, Application like interactive, Television, Video conferencing, Video on demand.

VR devices: Hand Gloves, Head mounted tracking system, VR chair, CCD, VCR, 3D Sound system, Head mounted display. Virtual Objects, Basics of VRML.

Uses of animation, types of animation, principles of animation, Techniques of animation: Onion Skinning, Motion Cycling, masking, Flip Book animation, Rotoscoping & blue-screening, color cycling, morphing, animation on the web, 3D animation.

B. Creating animation using Flash, 3D-Max.

Text Books

Reference Books
Additional Reading
CS42113 :: DIGITAL SIGNAL PROCESSING

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Mathematical Transforms and Applications

Objectives:
- To understand how to analyze and manipulate digital signals in various domains and design digital filters.
- Mapping with PEOs :- II, III, (c)

Unit I  
Introduction to signals and systems  
(8+1 Hrs)

B. Properties of LTI systems, parallel and cascade connection, Correlation of DTS.

Unit II  
Z and Fourier transforms  
(8+1 Hrs)

B. Symmetry properties of F. T, F. T. theorems: Linearity, time shifting, frequency shifting, time reversal, differentiation.

Unit III  
(8+1 Hrs)

Structure & Syllabus of  B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
Frequency analysis of Signals and Systems

A. Frequency Response of LTI Systems: Ideal frequency selective filters, magnitude and phase response, group delay, Frequency Response for Rational System Functions: Frequency Response of a single zero or pole, Frequency response from pole-zero plot using simple geometric construction

Sampling the F.T., Fourier representation of finite-duration sequences, The Discrete Fourier Transform, Properties of DFT: circular shift, duality, symmetry, Circular Convolution, Linear Convolution using DFT, Effective computation of DFT and FFT, DIT FFT, Overlap and save algorithm, Inverse DFT using FFT(DIF)

B. Properties of DFT: Linearity, DIF FFT, Goertzel Algorithm, Inverse DFT using FFT(DIT)

Unit IV  (9+1 Hrs)
Design of Digital Filters

A. Concept of filtering, Ideal filters and approximations, specifications, IIR filter design from continuous time filters: Characteristics of Butterworth, Chebyshev approximations, impulse invariant and bilinear transformation techniques, Design examples, FIR filter design using windows: properties of commonly used windows, systems with Linear phase, Generalized Linear phase systems, Four Types of GLPS (Type I), Design Examples, Design using Kaiser window, Comparison of IIR and FIR Filters

B. Four Types of GLPS (Type II,III,IV), Examples on Filter Design (IIR & FIR)

Unit V  (7+1 Hrs)
Realization of Filters

A. Block diagrams and Signal flow graph representation of LCCDE, Basic structures for IIR Systems: direct form, cascade form, parallel form, feedback in IIR systems, Basic Structures for FIR Systems: direct form, cascade form, structures for linear phase FIR Systems, Detail Study of DSP chip architecture as an example of ADSP 21XX series and their desirable features.

B. Instruction set of ADSP 21XX series processor and some examples.

Text Books

Reference Books

CS42114:: PRODUCT DESIGN

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To understand design and development process for products
- To study effect of product development on business plans, team work and growth.
- Mapping with PEOs :- II, III, (c)

Unit I (9+1 Hrs)
Introduction and User Studies


B. Selection of a Product for Design Study such as Antivirus, Operating System, Mobile Phones, Web Browsers, Accounting Software etc.

Unit II (7+2 Hrs)
Design and Usability Evaluation


B. Ethnographic and Cultural Study of Selected Product.

Unit III (8+2 Hrs)
Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’, Issue 03, Rev01 dated 02/04/2011
Categorization of Products

A. Products for Future Use, Products to be Used in Groups, Devices used in Public Places, Products that Enrich User Experience, Embedded Products, Designer Products, Interfaces, Complexity of Interfaces, Design of Multi-Modal Interfaces, Expressive Interfaces, Natural Interfaces, Tangible Interfaces, Faulty Interfaces.

B. Classification of Selected Products and Possible Variances with Extended Features.

Unit IV  
(8+2 Hrs)

Design Management and Professional Practice


B. Identification of IPR (Copyrights, Patents and Trademarks) Issues with Selected Products.

Unit V  
(8+1 Hrs)

Product Life and Marketing


B. Analysis of Prices and Related Pricing Policy for Selected Product.

Text Books


Reference Books


**Additional Reading**

CS42116:: CONVERGENCE TECHNOLOGIES

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Computer Networks.

Objectives:
- To understand need of convergence.
- To learn various networks, and signaling in that network.
- To understand voice and data communication.
- Mapping with PEOs :- II, III, (c)

Unit I  (7+2 Hrs)
Introduction to Convergence

A. what is network Convergence, the promise of network convergence, networking issues and convergence, Voice and data network characteristics, benefits of IP centric network, challenges of converged network, introduction to VOIP, applications of converged networks, VOIP implementation challenges.
B. voice and data network growth factor, effects of network convergence on businesses.

Unit II  (9+1 Hrs)
Protocols and Standards for Convergence

A. Protocols Supporting VOIP: Multicast IP, RTP, RTCP, RSVP, RTSP, SDP, SAP, SIP.  
Subscriber Lines: T1/T3, DS0, DS1, DS3, E1/E3.  
Signaling Standards: H.323, SIP.  Gateways, Gatekeepers.
B. MGCP, Audio and Video Codecs.

Unit III  (9+1 Hrs)
Switching networks

A. ISDN: conceptual view of ISDN, transmission structure, user-network interface configuration, ISDN Protocol Architecture, ISDN connection, Addressing, Interworking, PRI, BRI, LAPD,  
Basic Call control, SS7.  
B-ISDN standards, Broadband services, B-ISDN architecture, B-ISDN protocol reference  
B. ISDN standards, SDH.
Unit IV
Frame Relay and SMDS

A. Frame Relay Circuits, Frame mode protocol architecture, frame mode call control, LAPF, Congestion in frame relay networks, approaches to congestion control, Traffic rate management, Explicit congestion avoidance, implicit congestion control.

SMDS: introduction to SMDS, SMDS interface protocol, SMDS addressing.

B. Comparison of SMDS with other LAN technologies.

Unit V
ATM technology

A. ATM VPI& VCI, Creation of virtual channel, Definitions of Virtual circuit and permanent virtual circuit, ATM reference model, step-by-step PVC example of how ATM network processes cells, AAL, Adaption layer from voice over ATM perspective AAL1,AAL2, AAL3, Connection admission control (CAC). Cell Loss Priority (CLP), ATM and convergence technology.

B. ATM versus Frame relay, ATM versus SONET.

Text Books

Reference Books

Additional Reading
CS42118:: GEOGRAPHICAL INFORMATION SYSTEMS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil

Objectives:
- To increase student awareness of GIS technology and provide opportunities to process, analyze and visualize spatial data and information.
- To understand the complexities of data manipulation, analysis, and mapping at different scales of space, time, and complexity.
- To study applications of GIS and Remote Sensing.
- Mapping with PEOs :- I, V, ( j ).

Unit I  (8+1 Hrs)
GIS and Maps

B. Selection of a GIS Application in Various Domains such as Weather Forecasting, Urban Planning, Agriculture, Defense, Network Applications.

Unit II  (8+2 Hrs)
Remote Sensing Fundamentals

B. Study of Satellites such as IRS, OCEANSAT-1, IKONOS etc.

Unit III  (8+2 Hrs)
Image Processing

Unit IV (8+2 Hrs)
Spatial Data Modeling and Management

B. Design a Spatial Database for a Selected Application.

Unit V (8+1 Hrs)
Data Input, Quality and Analysis

B. Identification of Data Inputs Outputs and Study of Required Analytical Approach.

Text Books

Reference Books
Additional Reading

CS42122 : DESIGN AND ANALYSIS OF ALGORITHMS

Credits: 03  Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Data Structures and Files

Objectives:
- Fundamental understanding of the mathematics used to analyze, evaluate, and design algorithms
- Develop the ability to assess the advantages and disadvantages of different types of algorithms.
- Understand methods for designing time and space efficient algorithms.
- Increased ability to design and implement efficient solutions to problems.
- Proving problems NP-Completed and understand its impact in Computer Science
- Mapping with PEOs :- III, (e)

Unit I

Unit 1 : Overview of Time Complexity analysis, Divide and Conquer and Greedy Strategies.


Unit II

Dynamic Programming Strategy

A. General Strategy, Review of Multistage graphs, OBST and 0/1 Knapsack, Traveling Salesperson Problem, Shortest path in a Graph, Sequence Alignment problem, Scheduling problem.
B. String Editing Problem.

Unit III

Structure & Syllabus of B.E.(Information Technology) Program-Pattern ‘E11’, Issue 03, Rev01 dated 02/04/2011
Backtracking and Branch & Bound

A. **Backtracking**: General Strategy, n Queen’s problem, Graph Coloring, Hamiltonian Cycles, 0/1 Knapsack, Sum of Subsets

B. **Branch and Bound**: General Strategy, 0/1 Knapsack, Traveling Salesperson Problem

B. Postage stamp problem, n*n*n Queens problem.

**Unit IV**

**NP-Theory**


NP-Complete problems- Satisfiability problem, vertex cover problem, graph problems, scheduling, code generation problems, Dealing with NP completeness.

B. Simplified NP-Hard Problems.

**Unit V**

**Overview of Parallel Algorithms, Approximation algorithms and Randomized Algorithms.**


B. Preparata’s sorting algorithm, Approximation algorithm for graph coloring.

**Text Books**


**Reference Books**


CS42124:: ALGORITHMIC NUMBER THEORY AND ALGEBRA

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Basic introduction to linear algebra and abstract algebra (though it is not presumed) is helpful to better appreciate the contents of the course. First course on algorithms.

Objectives:

- Study basic number theory, various algebraic structures (such as groups, rings, fields, vector-spaces …etc) and their applications. These topics will be mainly studied in a computational perspective.
- Study various algorithms for several fundamental algorithmic questions in Number Theory and Algebra.
- Building up mathematical background and tools for further study in areas such as cryptography, computational number theory, computational complexity, coding theory.
- Mapping with PEOs I, II.

Unit I (8+2 Hrs)
Basic Number Theory
A. Infinitely many primes in Z(some proofs), greatest common divisor, unique factorization of integers, modular arithmetic, (some results on modular arithmetic: Fermat’s little theorem, Wilson’s theorem,… etc), linear congruences and Chinese Remaindering Theorem, linear Diophantine equations, some arithmetic multiplicative functions (e.g. Euler's phi function, Mobius function, divisor function…), Dirichlet convolution, Mobius inversion. Quadratic residues, Legendre symbol, Gauss’ law of Quadratic reciprocity. Distribution of primes, Prime number theorem.
B. More Diophantine equations (e.g. Pell’s equation), more results on distribution of primes(proving lower and upper bound(asymptotic) on pi(x), where pi(x) is number of primes upto x)

Unit II (8+1 Hrs)
Basic abstract Algebra
A. Groups, subgroups, Lagrange’s theorem, group homomorphism, quotient groups, normal subgroups, cyclic groups, Abelian groups and their decomposition, rings, ideals, some examples of rings (e.g. polynomial ring \( F[x] \), ring of integers modulo \( n \)), units and zero divisors in the ring, structure of unit group of \( \mathbb{Z}_n \), integral domains, fields, vector spaces. Unique factorization in ring \( k[x] \) where \( k \) is a field, Principal Ideal Domains (PID).

Basics of finite fields: existence of finite fields of size \( p^n \) for \( n > 0 \), \( p \) prime, isomorphism of finite fields of same size, structure of multiplicative group, field automorphisms, Frobenius maps.

B. Ring of Gaussian integers \( \mathbb{Z}[i] \) and Lagrange’s four square theorem, analogies between ring of integers and univariate polynomial ring, more properties of finite fields.

Unit III (8+2 Hrs)

Number theoretic algorithms

A. Euclid’s algorithm for GCD, Extended Euclid’s algorithm, fast integer arithmetic, algorithms for modular arithmetic, fast exponentiation, finding modular inverse, finding order of element in a group, finding quadratic non-residues, algorithm for modular square-root, algorithms for discrete-log problem.

Univariate polynomial arithmetic (multiplication, gcd, etc), irreducibility testing for polynomials in \( F[x] \) where \( F \) is finite field, ring \( F[x]/(f) \), modular inverse computation.

B. Chinese Remaindering and computing determinant of integer matrix, Euclid’s algorithm and Fibonacci numbers (a detailed analysis of Euclid’s algorithm), Some more algorithmic questions over finite fields.

Unit IV (9+2 Hrs)

Polynomial Factorization
A. **Univariate polynomial factorization over finite fields:** revisit unique factorization, Randomized polynomial time algorithms:

a. Berlekamp’s algorithm: linear algebra preliminaries for Berlekamp’s algorithm, Frobenius map, the algorithm, resultants and some implications of Berlekamp’s algorithm, parallel algorithm for computing gcd of polynomials.

b. Cantor-Zassenhaus algorithm: distinct degree and equal degree factorization, CRT and Cantor-Zassenhaus algorithm.

**Geometry of Numbers and polynomial factorization over rationals:** Basics on integer lattices, Minkowski’s theorems, algorithmic questions over integer lattices (Closest Vector Problem and Shortest Vector Problem) Gauss’ algorithm for SVP in 2 dimensions. Lenstra-Lenstra-Lovasz(LLL) approximation algorithm for SVP. Deterministic polynomial time algorithm for factorization of univariate polynomials over rational(using LLL and Hensel lifting lemma)

B. Reduction of polynomial factorization problem to root finding using Berlekamp’s algorithm. Recent deterministic sub-exponential time algorithm for the problem.

**Unit V** *(7+1 Hrs)*

**Primality**

A. Pratt’s result: PRIMES in NP intersection coNP, randomized polynomial time algorithms:

a. Miller-Rabin primality test: Carmichael numbers, derandomizing Miller-Rabin test using Riemann Hypothesis, computing a factor of a number given phi(n) where phi is Euler’s totient function.

b. Solovay-Strassen primality test: Jacobi symbol, quadratic reciprocity, Solovay-Strassen primality test.

Prime number theorem and generating large primes (application to RSA crypto system)

B. Agarwal-Kayal-Saxena(deterministic polynomial time) primality test.

**Text Books**

1. Modern Computer Algebra by Joachim von zur Gathen, Jürgen Gerhard (Cambridge)

2. A computational introduction to Number Theory and Algebra by Victor Shoup(Cambridge)

3. A classical introduction to modern number theory by Ireland and Rosen
Reference Books

1. Topics in Algebra by I. N. Herstein (Wiley Publishing company)

2. Algebra by Michael Artin (Pearson Prentice Hall)
CS 40204 :: SOFTWARE TESTING & QUALITY ASSURANCE

Credits: 01

Teaching Scheme: - - 1 Hr/Week

Prerequisites: Software Engineering

Objectives:
- To understand Software Measurement Theory and Software Test Automation
- To practice Software Testing Techniques and Strategies
- Understand the fundamental concepts, approaches, and methodologies in software quality management and assurance.
- Understand the framework and general approach of several Quality System Standards and Total Quality Management (TQM).
- Mapping with PEOs :- III, (e)

List of Contents

A TERM-WORK containing the record of the following:

1. To Prepare Test Plan for the given problem. The Test plan consists of following issues.
   a. Purpose of the test.
   b. Location and schedule of the test.
   c. Test descriptions.
   d. Pass and Fail Criteria.
2. To identify and narrate Test cases, Test scripts/procedures. To learn Test suite in manual testing using Rational Test Manager tool to explore testing basic concepts.
4. To perform Unit testing especially indicating the traced Independent data paths, Control paths and Error handling paths. Prepare control flow graphs for the unit under test. Compute the Cyclomatic complexity of the unit.
5. To perform Data Flow testing for the Program Segments by identifying the Definition-Use chain and type of data flow anomaly.
6. To perform Mutation Analysis of the Program Segment along with mutant history, mutation score and type of mutation by using any Code analysis Tool (JUnit).
7. To perform Black-Box Testing for all the units contained in the architectural segments using Equivalence Partitioning, Boundary Value Analysis and
Orthogonal Array testing methods.
8. To study exploratory Testing for the Module under Test and merits/demerits of this technique.

Text Books

Reference Books

Additional Reading
CS42213:: DIGITAL SIGNAL PROCESSING

Credits: 01

Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites:: Nil

Objectives:
- To understand how to analyze and manipulate digital signals in various domains
- Mapping with PEOs :- II, III, (c)

List of Contents

A TERM-WORK containing the record of the following:

Assignments :

1 Verification of sampling Theorem:
2 Linear Convolution
3 GDE
4 Correlation.
5 Pole Zero Implementation
6 Magnitude and Phase response of a system
7 DFT
8 Circular convolution
9 FFT Algorithms (DIT/DIF)
10 Design IIR filter using analog filter approximations
11 Designing FIR filters using windowing techniques
Students should implement the above assignments in Matlab.

**Text Books**

**Reference Books**
CS42214:: PRODUCT DESIGN

Credits: 01  
Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: : Nil

Objectives:
  • To identify and incorporate key social, cognitive and physiological factors that influence user perceptions, understandings and usages of Information Technology.
  • To motivate students for interviews and field-work with users for improved practical approach in interface/interaction based product design.
  • Mapping with PEOs :- II, III, (c)

List of Contents

A TERM-WORK containing the record of the following:

Design a questionnaire for study of selected product.

1. Study specialized user population and their impact on the design of selected product.
2. Perform a GOMS analysis for any task(s) related with selected product.
3. Study faulty interfaces/interactions related with selected product.
4. Analyze the cybercrimes related with selected product and suggest preventive measures.

Text Books

Reference Books

Additional Reading
CS42216:: CONVERGENCE TECHNOLOGIES

Credits: 01

Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: : Nil

Objectives:

- To understand the next generation technology.
- Understand the switching techniques.
- Mapping with PEOs :- II, III, (c)

List of Contents

A TERM-WORK containing the record of the following:

Assignments :

1. Study of audio and video data.
2. Signal passing through network for voice data.
4. Study of gatekeepers in convergence technology.
5. Study of ISDN devices.
7. Study of SS7 implementation.
8. Comparison of ISDN and B-ISDN.
9. Use of frame relay in convergence.
10. Examples on convergence

Text Books


Reference Books

Additional Reading
CS42218::GEOGRAPHICAL INFORMATION SYSTEMS

Credits: 01

Teaching Scheme: - Tutorial  1 Hrs/Week

Prerequisites: Nil

Objectives:
- To study applications of GIS & Remote Sensing
- To study Data Modeling and Analysis required by these systems.
- Mapping with PEOs :- I, V, (j).

List of Practical

A TERM-WORK containing the record of the following:

1. Prepare a map for the selected geographical area as per topological survey.

2. Design a spatial database for the entities related with a geographical area specified in above map.

3. Study the connectivity of maps with spatial databases.

4. Analyze a case study of any GIS application of your choice.

5. Prepare a presentation on any latest GIS technology / technique / software / hardware.

Text Books

Reference Books
Additional Reading

CS4222:: DESIGN AND ANALYSIS OF ALGORITHMS

Credits: 01

Teaching Scheme: - Tutorial 1 Hrs/Week

Prerequisites: Data Structures and Files

Objectives:
• Fundamental understanding of the mathematics used to analyze, evaluate, and design algorithms
• Develop the ability to assess the advantages and disadvantages of different types of algorithms.
• Understand methods for designing time and space efficient algorithms.
• Increased ability to design and implement efficient solutions to problems.
• Proving problems NP-Completed and understand its impact in Computer Science
• Mapping with PEOs :- III, (e)

A Term work containing the following record:

2. Find the median using divide and conquer approach. Time complexity measure is to be obtained.
3. Minimal spanning Trees/ Job scheduling as an example of Greedy approach
4. Finding shortest path for multistage graph problem. (single source shortest path and all pairs shortest path.)
5. OBST/Flow Shop Scheduling as an example of dynamic programming.
8. 8-Queen problem/ Graph coloring problem: general backtracking method and recursive back tracking method and their comparison for space and time complexity.

10. Algorithm implementation for ‘Traveling salesman' problem using -
    Dynamic programming approach.
    Branch & Bound approach.


12. Randomized Quick Sort and its analysis.

Text Books


Reference Books


CS42224:: ALGORITHMIC NUMBER THEORY AND ALGEBRA

Credits: 01

Teaching Scheme: - Tutorial 1 Hr/Week

Prerequisites: Basic introduction to linear algebra and abstract algebra (though it is not presumed) is helpful to better appreciate the contents of the course. First course on algorithms.

Objectives:

• Study basic number theory, various algebraic structures (such as groups, rings, fields, vector-spaces …etc) and their applications. These topics will be mainly studied in a computational perspective.

• Study various algorithms for several fundamental algorithmic questions in Number Theory and Algebra.

• Building up mathematical background and tools for further study in areas such as cryptography, computational number theory, computational complexity, coding theory.

• Mapping with PEOs I, II.

List of Contents

A TERM-WORK containing the record of the following:

1. Problem solving based on topic like prime numbers, gcd of integers, some simple number theoretic questions

2. Modular arithmetic, Chinese remainingdering applications

3. Multiplicative functions, analytical estimates of some number theoretic functions

4. Problem solving ( based on topics Groups, subgroups, homomorphism, cyclic groups etc)

5. Ring, ideals, some examples of commutative rings

6. Vector spaces
7. Integral domains, finite fields

8. More on finite fields, automorphisms, Frobenius maps, some algorithmic questions..

9. Ring of univariate polynomials \( F[x] \), quotient ring \( F[x]/(f) \)

10. Berlekamp’s algorithm and more

11. Integer lattices

12. Carmichael numbers, Lagrange symbol, Jacobi symbols, Primality testing.

**Text Books**

1. Modern Computer Algebra by Joachim von zur Gathen, Jürgen Gerhard (Cambridge)

2. A computational introduction to Number Theory and Algebra by Victor Shoup (Cambridge)

3. A classical introduction to modern number theory by Ireland and Rosen (Springer)

**Reference Books**

1. Topics in Algebra by I. N. Herstein (Wiley Publishing company)

2. Algebra by Michael Artin (Pearson Prentice Hall)
CS40308 :: NETWORK SECURITY

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Nil

Objectives:

- Analyze the packets
- To Understand various attacks
- To able to do encryption and decryption
- Mapping with PEOs :- IV, V, (c)

List of Practical

2. Study of antivirus tool.
4. Implement packet sniffer.
5. Demonstrate: ARP spoofing, IP spoofing.
6. Implementation of DES.
7. Implementation of RSA.
8. Comparison of encryption time and decryption of RSA, DES, ECC.
10. Implement Hash algorithm.
11. Implementation of email security using PGP.
Text Books

Reference Books

Additional Reading
CS 42307:: SOFTWARE DESIGN AND ARCHITECTURE

Credits: 01  
Teaching Scheme: - Laboratory  2 Hrs/Week

Prerequisites: Software Engineering

Objectives:
- To describe what patterns are, how to use them, and why they are important.
- To understand contribution of patterns towards the design process efficiency
- To trace the relationship between patterns and traditional methods
- To learn the relationship among patterns, frameworks, object-oriented languages, and software architecture
- To identify fundamental and advanced concepts of design and architectural patterns
- To Structure systems by applying architectural patterns
- To focus on Archetypes and Architecture Documentation
- Mapping with PEOs :- I,V, (j)

List of Practical

1. Select a moderately complex system and narrate concise specification for the same. Implement the system features using Abstract Factory and Composite design patterns. State the complete pattern specification and note the difference between the patterns.

2. Select a moderately complex system and narrate concise specification for the same. Implement the system features using Facade and Proxy design patterns. State the complete pattern specification and note the difference between the patterns.

3. Select a moderately complex system and narrate concise specification for the same. Implement the system features using Flyweight and Iterator design patterns. State the complete pattern specification and note the difference between the patterns.

4. Select a moderately complex system and narrate concise specification for the same. Implement the system features using Template and Command design patterns. State the complete pattern specification and note the difference between the patterns.
5. Select a moderately complex system and narrate concise specification for the same. Implement the system features using Mediator and Observer design patterns. State the complete pattern specification and note the difference between the patterns.

6. Select a complex system and narrate concise specification for the same. Develop architecture specification and use archetypes to recognize the architectural elements.

7. **Text Books**

**Reference Books**

**Additional Reading**
CS42301:: ADVANCED COMPUTER GRAPHICS

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Nil

Objectives:

• To gain first-hand experience with the challenges of efficient and accurate modeling, rendering, and simulation, and the necessary data structures and algorithms.
• To introduce a current 3D graphics API.
• To develop programming skills in 3D computer graphics.
• Become acquainted with some advanced topics in computer graphics; these might include texturing, physically-based modeling, procedural modeling, curves and surfaces, global illumination, interaction, and visualization.
• Mapping with PEOs :- V, (j)

List of Practical

1. Implement an OpenGL program to draw different 2D shapes.
2. Implement an OpenGL program to draw 2 overlapped shapes and use alpha blending.
3. Implement an OpenGL program to draw 3D cube and apply transformations.
4. Implement an OpenGL program to draw 12 spheres and apply different light effects.
5. Implement an OpenGL program to draw scene and apply fog effect.
6. Implement an OpenGL program to draw 3D cube and apply different textures on different faces.
7. Implement CUDA program for the prefix addition.
8. Implement CUDA program for the multiply two matrices.
10. Implement a program for edge detection using Gaussian filter.

**Text Books**

**Reference Books**

**Additional Reading**
CS42308:: SYSTEM PROGRAMMING

Credits: 01

Teaching Scheme: -Laboratory 2 Hrs/Week

Prerequisites: Nil

Objectives:
1. To understand the concepts and components of Systems Programming
2. To Learn and understand the fundamentals of Operating systems
3. Mapping with PEOs :- II, III, (d)

List of Practical

1. Implementation of Macros.
2. Implementation of Nested macros.
3. Design and implementation of 1 pass assemblers.
4. Design and implementation of 2 pass assemblers.
5. Design of an Editor: Design of a Line or Screen Editor using C Language.
7. Design Lex specifications for the tokens – keywords, identifiers, numbers, operators, white spaces.
8. Implementation of Toy-code generator.
10. Simulation of loaders.
11. 3-4 assignments on DLL on Linux shared library.
12. Use of different debugger tools.

Text Books


Reference Books

1. “Introduction to 3D Game Programming with Direct X 9.0c: A Shader Approach”,
   Frank Luna, Jones and Bartlett publication, ISBN- 1598220160
CS42310:: MULTIMEDIA SYSTEMS

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Nil

Objectives:
- To implement basic concepts of computer graphics.
- To implement algorithms to draw various graphics primitives.
- To implement 2-D and 3-D transformations.
- To implement applications of multimedia
- Mapping with PEOs :- II, III, (c)

List of Practical

1. Write a code in c to read the contents of any .bmp file and display image.
2. Write a menu Driven program
   a) Create, edit .Voc file and convert into .Wav file format
   b) Create, edit .Wav file and convert it to .Voc file format.
3. Create an animation using 3-D Max
4. Create an animation using Flash
5. Write a tool to create presentation slide with audio & video effects
6. Using VRML generate anyone virtual scene
   1. Coffee House
   2. Building Model
   3. Garden Model
7. Implement Edge Detection Algorithm on any Image
8. Implement arithmetic coding technique on any image.
9. Implement JPEG-.DCT compression Technique.
Text Books

Reference Books

Additional Reading
CS47308 :: PROJECT STAGE 3

Credits: 06  
Teaching Scheme: - 8 Hrs/Week

Aim

This course addresses the issues associated with the successful management of a software development project. The course emphasizes project life cycle phases, requirement engineering, system analysis and system design. A further aim is for students to heighten personal awareness of the importance of developing strategies for themselves and their career. The Project Work can lead to:

f. Transform existing systems into conceptual models.
g. Transform conceptual models into determinable models.
h. Use determinable models to obtain system specifications.
i. Select optimum specifications and create physical models.
j. Apply the results from physical models to create real target systems.

Objectives

1. To provide an understanding of the project planning process and problem scoping
2. To define roles and work with cross functional teams
3. To establish clear project objectives and milestones
4. To create effective and deliverable project plans
5. To perform rigorous analysis and design activities for solution planning
6. To select appropriate technology for implementation and testing

Overview of the Course:

1. The Student Project Group will prepare a detailed Project Report consisting of Semester I Preliminary Project document along with Detailed System Design Document, Implementation and Testing Document with conclusion and future scope of the Project Work. All the documents indicated will have a prescribed format. The Project Report ideally should consist of following documents: (Exceptions may be there based on the nature of the project, especially if some of the following documents are not applicable to particular project as determined by the project guide, coordinator and head of department.)
<table>
<thead>
<tr>
<th>Sr.</th>
<th>Project Item</th>
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</thead>
<tbody>
<tr>
<td>1</td>
<td>Project Cover Front Page</td>
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<tr>
<td>2</td>
<td>Project Completion Certificate [Institute]</td>
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<tr>
<td>3</td>
<td>Project Completion Letter [In case of Sponsored Projects]</td>
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<td>Table of Contents</td>
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<td>7</td>
<td>List of Tables</td>
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<tr>
<td>8</td>
<td>Project Synopsis [Problem Background, Existing System Details, Proposed Solution]</td>
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<tr>
<td>9</td>
<td>Feasibility Study Report</td>
</tr>
<tr>
<td>10</td>
<td>Project Plan</td>
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<tr>
<td>11</td>
<td>System Requirement Specification</td>
</tr>
<tr>
<td>12</td>
<td>System Analysis Document: UML Use Case Diagrams</td>
</tr>
<tr>
<td>13</td>
<td>System Analysis Document: UML Sequence Diagrams</td>
</tr>
<tr>
<td>14</td>
<td>System Analysis Document: UML State Diagrams</td>
</tr>
<tr>
<td>15</td>
<td>System Design Document with Module Specifications</td>
</tr>
<tr>
<td>16</td>
<td>System Implementation</td>
</tr>
<tr>
<td>17</td>
<td>System Testing and Experimental Findings</td>
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<tr>
<td>18</td>
<td>Conclusion</td>
</tr>
<tr>
<td>19</td>
<td>References</td>
</tr>
</tbody>
</table>

2. The Project Work will be assessed jointly by a panel of examiners consisting faculty and industry experts. The Project Groups will deliver the presentation and demonstration of the Project Work which will be assessed by the panel.

3. The Student Project Group needs to actively participate in the presentation. The panel of examiners will evaluate the candidate’s performance based on presentation skills, questions based on the Project Work and overall development effort taken by the candidates.

**Note:**
The student needs to design and develop solution for the identified technological problem in the area of Computer Engineering or Information Technology of their choice. The Project Implementation needs to be completed using best possible use of available technologies as applicable to deal with the complexity of the project. The Project Group will prepare a detailed report of the project work which will be approved by the concerned faculty member. The Project Report need to be submitted both in Hard form
and Soft form in CD. The Soft Copy of the Project Report must accompany other project deliverables as well.

**Assessment Scheme**

<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Content</th>
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<tbody>
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<td>System Requirement Specification</td>
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<tr>
<td>2</td>
<td>Feasibility Study</td>
<td>05</td>
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<tr>
<td>3</td>
<td>System Analysis</td>
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<tr>
<td>4</td>
<td>System Design</td>
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<tr>
<td>5</td>
<td>System Implementation</td>
<td>25</td>
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<tr>
<td>6</td>
<td>System Testing</td>
<td>25</td>
</tr>
<tr>
<td>7</td>
<td>Presentation of the Project Work</td>
<td>20</td>
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</tbody>
</table>

**Outcomes:**

Upon completion of the course, the students will be to:

1. Identify major issues in complex situations; and know how to prepare alternative solutions and make decisions.

Becoming aware of the need to think and act in an entrepreneurial manner by developing the ability to: critically and creatively understand innovations and development, work independently and collaboratively.
## Structure, HONORS (COMPUTER ENGINEERING)

FF653, Issue No. 3, Rev 1, dt 02/04/2011

<table>
<thead>
<tr>
<th>Subject No.</th>
<th>Subject Code</th>
<th>Subject Name</th>
<th>Lect.</th>
<th>Tutorial</th>
<th>Practical</th>
<th>Credits</th>
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<tr>
<td>S₁, p₁</td>
<td>CS28102</td>
<td>Unix Programming</td>
<td>2</td>
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<td>Advanced Data Structures</td>
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<td>S₃</td>
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</tbody>
</table>
CS28102:: UNIX PROGRAMMING

Credits: 02  Teaching Scheme: - Theory 2 Hrs/Week

Prerequisites: Familiarity with Computer Operations, C Programming.

Objectives:
- Teaches the fundamentals of Unix Operating System.
- Prepares the student for Unix Administration.
- Prepare the students for Shell Programming, AWK Programming.
- Mapping with PEOs :- II, III, (d)

Unit I  (5+1 Hrs)
Introduction
B. Comparison of Windows with Unix.

Unit II  (5+1 Hrs)
Unix Commands
A. Basic commands: Internal and External Commands, Command Structure, man, cat, cal, date, passwd, echo, printf, less, more, wc, bc, uname, who, tty, clear, script.
File & Directory Manipulation under Unix: File Concepts, cat, cp, rm, mv, more, file, wc, od, cmp, diff, dos2unix, unix2dos, mkdir, rmdir, cd, ls, pwd.
B. Input and output redirection

Unit II  (5+1 Hrs)
File permissions, Filters and vi Editor
Filter and Redirection: Concepts, pr, head, tail, cut, paste, sort, uniq, tr, tee, grep, Pipe and I/O Redirection.
vi Editor: Creating and Viewing Files using the vi Editor, vi commands
B. Sed: The Stream Editor
Unit IV
Shell Programming

A. Shell Programming: Read Statement, Command Line Arguments, Different Operators
Control Structures: if, for, while, case.
Positional Parameters, Arrays, Functions, Writing Shell Scripts, Debugging.
B. Comparison of different Shell Scripting.

Unit V
AWK Programming

A. AWK Programming: Format of AWK Programs, Records, Fields, Variables,
Variables, Expressions, Operators, Print Statement, Control Statements in Actions,
BEGIN and END block. Control Structures: if, for, while.
AWK Functions: String Handling Functions, Mathematical Functions, Arrays
B. Passing Shell Arguments to AWK, Passing AWK Output to the shell.

Text Books

Reference Books

Additional Reading
CS38101:: ADVANCED DATA STRUCTURES

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Data Structures

Objectives:
- To introduce advanced data structures, problem solving paradigms
- To study the representation, implementation & applications of data structures.
- To compare alternative implementations of data structures.
- To choose the appropriate data structure for modeling a given problem.
- Mapping with PEOs: III, (e).

Unit I  (8+1 Hrs)
Hashing

B. Theoretical evaluation of overflow techniques.

Unit II  (8+2 Hrs)
Priority Queue and Advance Heaps

B. Symmetric Min-max Heaps.

Unit III  (9+1 Hrs)
Advanced Binary Search Trees

B+ Tree: Searching, Insertion, Deletion.
B. AA Trees. (9+1 Hrs)

Unit IV
Digital Search Structures

B. Searching a Suffix Tree.

Unit V
Data structures for Disjoint Sets and Linear Programming (8+1 Hrs)

B. Linear Programming duality

Text Books

Reference Books
CS38102:: ROUTING ALGORITHMS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Computer Networks.

Objectives:
- To explain routing algorithms in the Internet; link-state routing and distance-vector routing; broadcast and multicast routing algorithms.
- To explain path determination, strengths of link state and distance vector protocols in differing configurations.
- To describe the goals of routing protocols and convergence as impacted by different protocols.
- To explain the similarities and differences between several types of routing protocols.
- Mapping with PEOs :- IV,V, (i)

Unit I networking and Network Routing  
8 Hrs

B. ICMP, PING, DHCP, ARP, RARP SLIP, SONET, MPLS.

Unit II Shortest and Widest path Algorithms  
8 Hrs

A. Shortest Path and Widest Path: Bellman–Ford Algorithm and the Distance Vector Approach, Dijkstra’s Algorithm, Widest Path Algorithm, Dijkstra-Based Approach, Bellman–Ford-Based Approach.
B. k-Shortest Paths Algorithm.

Unit III Routing Protocols: Framework and Principles  
8 Hrs


Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
B. Path Vector Routing.

Unit IV

Internet Routing Algorithms


B. BGP Message Format.

Unit V

Analysis of Network Algorithms


B. IPv6

Text Books


CS48101: MODELING AND DESIGN OF COMPUTER NETWORKS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Computer Networks.

Objectives:
- To understand analysis of a problem, design of its solution, implementation of the solution, testing of the solution.
- To describe the methods used in modeling, analysis and design communications systems.
- To describe the organization of computer networks and evaluate alternative organizations.
- To evaluate the protocols used in computer networks.
- Mapping with PEOs: III, IV, (f)

Unit I
Introduction to Queuing Theory  
(8 Hrs)
A. Multiplexing of Traffic on a Communication Link, Queuing Models- Little’s Theorem, Little’s Theorem, Probabilistic Form of Little’s Theorem, Application of Little’s Theorem, The M/M/1 Queuing System, Arrival Statistics, Service Statistics, Markov Chain Formulation, Deviation of the Stationary Distribution, Occupancy Distribution upon Arrival, Occupancy Distribution upon Departure, The M/M/m, M/M/∞, M/M/m/m. AND Other Markov Systems, The M/M/m: The m-Server Case, M/M/∞: The Infinite-Server Case, M/M/m/m: The m-Server Loss System, multidimensional Markov Chains- Applications in Circuit Switching, The M/G/1 System, M/G/1 Queues with Vacations, Reservations and Polling, Priority Queuing, The D/D/1 Queue.
B. Problems on queuing theory.

Unit II
Delay Models in Data Networks  
(8 Hrs)
A. M/M/1 queue - Time-dependent behavior, Limiting behavior, Direct approach Recursion, Generating function approach, Global balance principle, Mean performance measures, Distribution of the sojourn time and the waiting time, Priorities, Preemptive-resume priority, Non-preemptive priority, Busy period, Mean busy period, Distribution of the busy period.
B. problem on M/M/1 system for related topics.

Unit III (8 Hrs)
Network Design

B. Problems on CMST, Bin packing, Terminal Assignment, Concentrator location.

Unit IV (8 Hrs)
Optical network design

B. WDM-PON, Heuristic Adaptation Algorithm for Larger Networks.

Unit V (8 Hrs)
Survivable optical network

B. Advanced Topics in Network Survivability, Virtual Concatenation.

Text Books

Reference Books
Additional Reading

CS48102:: MOBILE ADHOC NETWORKS

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Computer Networks.

Objectives:
- To understand MANET and WSN.
- To learn various routing algorithms used in MANET.
- To understand security mechanisms in MANET.
- Mapping with PEOs :- I, V, ( j )

Unit I  
Introduction  
(8 Hrs)


B. Challenges Facing Ad hoc Mobile Networks, Ad hoc wireless Internet.

Unit II  
Medium Access Control  
(8 Hrs)


Unit III  
Ad Hoc Routing Protocols  
(8 Hrs)


Unit IV  \((8\ \text{Hrs})\)
Multicast Routing In Ad hoc Networks


Unit V  \((8\ \text{Hrs})\)
Transport Layer And Security Protocols


Text Books

Reference Books
CS28302: UNIX PROGRAMMING

Credits: 01

Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Operating Systems, Familiarity with Computer Operations, C Programming

Objectives
- Teaches the fundamentals of Unix Operating System.
- Prepares the student for Unix Administration.
- Prepare the students for Shell Programming, AWK Programming.
- Mapping with PEOs: - II, III, (d)

List of Practical

1. Installation of Linux
2. Execution of basic Linux commands.
3. Execution of advanced Linux commands.
5. Shell Program to check file permissions
6. Shell Program to check string is palindrome or not.
7. Shell Program to perform arithmetic operations using case statement
8. Shell Program for Bubble Sort using Array.
10. Execution of AWK related commands.
11. Generate a student report using AWK programming
12. AWK program for passing shell arguments and Passing AWK Output to the shell.
13. Managing Users and Groups and Basic Network Setup

14. Execution of C & Java program on Unix.

Text Books

Reference Books

Additional Reading
MINORS
Structure, MINORS (COMPUTER ENGINEERING)

<table>
<thead>
<tr>
<th>Subject No.</th>
<th>Subject Code</th>
<th>Subject Name</th>
<th>Teaching Scheme (Hrs/week)</th>
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<td>CS29102</td>
<td>Principles of Programming Languages</td>
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CS29102:: PRINCIPLES OF PROGRAMMING LANGUAGES

Credits: 02  Teaching Scheme: - Theory 2 Hrs/Week

Prerequisites: C

Objectives:
- To understand the concept of Interfaces, Packages.
- To understand the concept of Exception Handling and Multithreading
- To understand the concept of Applets and AWT.
- Mapping with PEOs :- II, III, (d)

Unit I  (5+1 Hrs)
Introduction to Programming languages & OOP (Java)

A. Introduction: Role of programming languages, Necessity of studying programming languages, characteristics of a good programming language, Effects of Environments on languages (batch Processing, Interactive, Embedded, Programming Environment) Language design issues, Programming paradigms.

   Introduction to JAVA: Classes & Objects – Constructors, Access Modifiers, Instance Methods, this & static keywords. Inheritance – Types of inheritance, Constructors in Derived Classes, Overriding & Hiding Fields & Methods,

B. Multilevel Inheritance, Hierarchical Inheritance.

Unit II  (5+1 Hrs)
Extended Object Oriented Programming (Java)


B. User defined exception

Unit III  (5+1 Hrs)
Java AWT & SWING
B. Concurrent Issues with thread programming, Deadlock.

Unit IV (5+1 Hrs)
Java Database Connectivity
A. overview of JDBC, ODBC, creating DSN, connecting to database using JDBC: ODBC, prepared statement, handling record/result sets, using database on remote machine.
B. SQL injection

Unit V (5+1 Hrs)
Case Studies of Programming Languages
A. Overview of the building blocks of the language, procedures, control structures, their motivation(s), target user base, choice and paradigms of features, special features relevant to HTML-CSS-JavaScript, PHP-HTML, Matlab.
B. LISP /Haskell.

Text Books

Reference Books

Additional Reading
CS29302:: PRINCIPLES OF PROGRAMMING LANGUAGES

Credits: 01  Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: C

Objectives:
- To understand basic concepts of Object Oriented Programming.
- To understand inheritance, polymorphisms, templates, file handling.
- To understand the concept of Interfaces, Packages.
- To understand the concept of Exception Handling and Multithreading
- To understand the concept of Applets, AWT and SWING
- Mapping with PEOs :- II, III, (d)

List of Practical

1. Write a simple JAVA program to implement the concept of Class & Object.
2. Write a simple JAVA program to implement the concept of Class, Constructor, instance variable & class variable.
3. Write a JAVA program to implement the concept of inheritance.
4. Write a JAVA program to implement the concept of interface.
5. Write a JAVA program to implement the concept of package.
6. Write a JAVA program to illustrate following exceptions
   a. Arithmetic Exception
   b. NullPointerException
   c. ArrayIndexOutOfBoundsException
   d. IllegalAccessException
7. Write a JAVA program to illustrate the use of abstract class
8. Write a JAVA program to implement the concept of multithreading.
9. Write a JAVA program for file handling.
10. Design a simple applet application with event handling.

**Text Books**

**Reference Books**
CS39101:: COMPUTER ARCHITECTURE AND OPERATING SYSTEMS

Credits: 03

Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Nil.

Objectives:

- To understand the structure, function and characteristics of computer systems.
- To understand the design principle of the various functional units of digital computers.
- To identify the role of operating systems and explain the different structures of operating systems.
- To understand OS support for processes/threads, virtual memory, I/O and file systems.
- To evaluate processes and/or threads synchronization mechanisms and explain deadlock conditions and different ways to resolve them.
- Mapping with PEOs :- I,II,III, (b),(d).

Unit I
Structure of a Computer System & Processor Organization

A. Brief History of computers, Von Neumann Architecture, Functional Units, Data Types and Computer Arithmetic: Fixed and Floating point numbers, Signed numbers, Integer Arithmetic, 2’s Complement arithmetic, multiplication.

CPU Architecture (8086), Register Organization, Instruction types, Instruction formats, Instruction cycles, Types of operands, Addressing Modes.

B. IEEE standards for Floating point representations.

Unit II
Control Unit & I/O Organization

A. Single Bus CPU, Control Unit Operation: Instruction Sequencing, Micro-operations.


B. Applications of microprogramming, I/O channels
Unit III
Introduction to OS & Process Management

A. Architecture, Goals & Structures of O.S., Hardware Abstraction layer, Basic functions, Types of OS: Batch, multiprogramming, multitasking, time sharing.
Concurrency: Principles of Concurrency, Mutual Exclusion, Semaphores, Message Passing, Monitors

B. Sleeping Barber problem, Cigarette Smokers problem

Unit IV
Deadlock and CPU Scheduling

Multiprocessor Scheduling: Granularity, Design Issues, Process Scheduling

B. Real Time Operating System

Unit V
Memory Management

A. Memory Management requirements, Memory Partitioning: Fixed and Variable Partitioning, Memory Allocation: Allocation Strategies (First Fit, Best Fit, Worst Fit, Next Fit), Fragmentation, Swapping, Cache Memory.
Virtual Memory: Concepts, Segmentation, Paging, Address Translation, Page Replacement Policies (FIFO, LRU, Optimal), Thrashing, Demand paging.

B. Working Set model

Text Books

Reference Books

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011

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Additional Reading

CS39103:: COMPUTER GRAPHICS

Credits: 02

Teaching Scheme: - Theory 2 Hrs/Week

Prerequisites: Knowledge of C Programming and Basic Data Structures & Mathematics.

Objectives:
- To understand basics of computer graphics.
- To give more emphasis on implementation aspect of Computer Graphics Algorithm.
- To prepare the student for advance courses like multimedia / Image Processing.
- Mapping with PEOs :- I,II,(b)

Unit I
Graphics Primitives and Scan Conversions
(5+1 Hrs)

A. Introduction to computer graphics, Display Adapters, display modes, pixel, Frame Buffer, display file structure, display file interpreter. lines, line segments, Line generation, DDA and Bresenham’s line drawing algorithms and circle drawing by Midpoint and Bresenhams algorithms, Aliasing and Antialiasing.

B. Interactive devices: Tablets, touch panels, mouse, joysticks, track balls, light pen etc.

Unit II
Polygon and Clipping
(5+1 Hrs)

A. Types of Polygons, representation, Inside test of polygon, Polygon filling: Seed fill, Boundary fill, Scan line fill algorithm, Clipping: Introduction, viewing transformation, Line clipping : Cohen Sutherland algorithm, Polygon clipping : Sutherland Hodgeman algorithm, Generalized Polygon clipping, Text clipping.

B. Mid-Point Line Clipping algorithm.

Unit III
2D Transformations
(5+1 Hrs)

A. Introduction, Scaling, Rotation, Translation, matrix representation of 2D transformation, homogeneous coordinates, Reflection Transformations, Rotation about an arbitrary point, Shear transforms. Numerical Problems on transformation.

B. Normalized Device Coordinates.
Unit IV                      (5+1 Hrs)
3-D Transformations

A. Introduction, 3-D point representation, 3D Scaling and Translation, Matrix representation, Derivation of Rotation matrices along the main axis, Rotation about an arbitrary axis, Reflection along different axis, Numerical Problems on transformation.
B. Reflection along different Plane.

Unit V                      (5+1 Hrs)
Hidden lines and Curves

B. Warnock algorithm, Triadic Koch Curve.

Text Books

Reference Books
CS39303:: COMPUTER GRAPHICS

Credits: 01  
Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Knowledge of C Programming and Basic Data Structures & Mathematics.

Objectives:
- To understand basics of computer graphics.
- To give more emphasis on implementation aspect of Computer Graphics Algorithm.
- To prepare the student for advance courses like multimedia / Image Processing.
- Mapping with PEOs :- I,II, (b)

List of Practical

1. Implementation of DDA Line Drawing Algorithm.
2. Implementation of Bresenham’s Circle Drawing Algorithm.
3. Implementation of Scan Line polygon fill Algorithm.
4. Implementation of Cohen-Sutherland Line Clipping Algorithm.
5. Implementation of Sutherland-Hodgeman Polygon Clipping Algorithm.
7. Implementation of Triadic Koch Curve Algorithm.
8. Implementation of Fractal line and Surface Algorithm.

Marking Scheme:  70% for Continuous Assessment;
30% End Semester Practical Exam
Text Books

Reference Books
**CS39102:: ANALYSIS OF ALGORITHMS**

**Credits:** 03  
**Teaching Scheme:** - Theory 3 Hrs/Week

**Prerequisites:** Data Structures

**Objectives:**
- Fundamental understanding of the mathematics used to analyze, evaluate, and design algorithms
- Develop the ability to assess the advantages and disadvantages of different types of algorithms.
- Understand methods for designing time and space efficient algorithms.
- Increased ability to design and implement efficient solutions to problems.
- Mapping with PEOs :- III, (e)

**Unit I**

**Unit 1 : Overview of Time Complexity analysis, Divide and Conquer**


**Unit II**

**Greedy Strategy :**

B. Optimal Storage Problems.

**Unit III**

**Dynamic Programming Strategy**

A. General Strategy, Review of Multistage graphs, OBST and 0/1 Knapsack, Traveling Salesperson Problem, Shortest path in a Graph, Sequence Alignment problem,  
B. String Editing Problem.

Structure & Syllabus of B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011
Unit IV  
Backtracking and Branch & Bound  (8+1 Hrs)

A. **Backtracking:** General Strategy, n Queen’s problem, Graph Coloring, Hamiltonian Cycles, 0/1 Knapsack, Sum of Subsets  

B. **Branch and Bound:** General Strategy, 0/1 Knapsack, Traveling Salesperson Problem  

B. Postage stamp problem, n*n*n Queens problem.

Unit V  
NP-Theory  (8+1 Hrs)

NP-Complete problems- Satisfiability problem, vertex cover problem, graph problems, scheduling, code generation problems, Dealing with NP completeness.  
B. Simplified NP-Hard Problems.

Text Books  

Reference Books  
CS39104:: DATA STRUCTURES

Credits: 02
Teaching Scheme: - Theory 2 Hrs/Week

Prerequisites: Computer Programming

Objectives:
- To introduce fundamental data structures, problem solving paradigms
- To introduce time complexity analysis of problems.
- To study the representation, implementation & applications of data structures.
- To compare alternative implementations of data structures.
- To choose the appropriate data structure for modeling a given problem.
- Mapping with PEOs :- I, (a)

Unit I
Stack using Linear Data Structure

A. Polynomial representation using arrays, operations on polynomials like add, multiply, evaluate, Representation of sparse matrix, Addition of Sparse.


B. Expression Conversions and evaluation with respect to stack.

Unit II
Queue

A. Fundamentals of queue, Representation and Implementation of queue using arrays, Circular queue: representation and implementation, Applications of queue: Josephus Problem, Job Scheduling, Queue Simulation, Categorizing Data, Double Ended Queue, Priority queue.

B. Representation of multiple queues.

Unit III
Linked Representation
A. Dynamic Memory allocation, Array representation using dynamic memory allocation, Concept of linked organization, singly linked list, doubly linked list, circular linked list, Insertion, Deletion and traversal on above data structures. Displaying a Single Linked list in reverse way. Representation and manipulations of polynomials using linked lists.

B. Stack using Linked list.

Unit IV

Trees

A. Binary trees and its representation using sequential and linked organization, full and complete binary trees, Creation of a binary tree, binary tree traversals (recursive and non recursive), operations such as copy, equal etc. Binary search tree, creation of binary Search tree, finding height and counting leaf nodes of a binary search tree (with and without recursion), Finding mirror image of the binary search tree with and without recursion, Deletion of a node from a binary search tree. Printing a tree level wise and depth wise.

B. Heap sorting.

Unit V

Graph

A. Review of basic terminology, Representation of graphs using adjacency matrix, adjacency list, Traversals: Depth First and Breadth First, Kruskal’s and Prim’s algorithms for minimum spanning tree, Algorithm for shortest path- Dijkstra’s algorithm.

B. Graph applications : Multistage Graph Problem.

Text Books


Reference Books


Additional Reading


Structure & Syllabus of  B.E.(Information Technology ) Program-Pattern ‘E11’,Issue 03,Rev01 dated 02/04/2011

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CS39304:: DATA STRUCTURES

Credits: 01
Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Computer Programming

Objectives:
- To introduce fundamental data structures, problem solving paradigms
- To introduce time complexity analysis of problems.
- To study the representation, implementation & applications of data structures.
- To compare alternative implementations of data structures.
- To choose the appropriate data structure for modeling a given problem.
- Mapping with PEOs :- I, (a)

List of Practical

1. Write a C Program to represent single variable polynomial using array and perform addition, multiplication of them.

2. Write a C program to represent sparse matrix using array and perform sparse matrix addition.

3. Write a C program to convert infix expression to postfix and evaluate it using stack.

4. Write a C program to implement Circular Queue using array and perform add and delete operations on it.

5. Write a C Program to create a database (such as employee, student) using single linked list with options like Create, insert, delete, modify, search, print reverse, display etc.

6. Write a C program to accept binary numbers in doubly linked list & perform addition of them and store the result in another list.

7. Write a C Program to create two sorted singly linked lists, and Merge these two lists into third list without creating a new linked list.
8. Write a C program to create a binary search tree and its inorder, preorder and postorder traversal. Also perform insertion and deletion of a node in it.

9. Write a C program to create a binary search tree and find height & number of leaf nodes with and without recursion.

10. Write a C program to represent a given graph using adjacency array and find the shortest path using Dijkstra’s algorithms.

11. Mini project which will make use of different data structures learnt in this subject.

Marking Scheme:  
50% for Continuous Assessment;  
20% for Mini Project;  
30% End Sem. Practical Exam

Text Books

Reference Books

Additional Reading
CS49101:: SOFTWARE ENGINEERING AND PROJECT MANAGEMENT

Credits: 03  
Teaching Scheme: - Theory 3 Hrs/Week

Prerequisites: Data Structures

Objectives:
- To learn the complete Software life cycle and understand its major activities such as software requirement analysis, design, testing, and implementation.
- Understanding and Experience in Writing Requirements and Specifications.
- To thoroughly understand the practices of analysis and design (OOA and OOD)
- To understand the relative merits of the different UML diagrams
- Transforming analysis into design and relate it to implementation model
- Mapping with PEOs :- III, IV,(e),(f).

Unit I
Software Process Models and OO Methodologies
(8+1 Hrs)

B. Overview of OO Methodologies: OOAD, OOSE, OMT, DSDM

Unit II
Requirement Engineering and Model Driven Development
(8+1 Hrs)

Unit III

System Design Engineering

A. Design quality, Design Concepts, The Design Model, Introduction to Pattern-Based Software Design, Architecture styles, Reference Architectures Architectural Design: Software Architecture, Data Design and Architectural Design, Design of Software Objects, Features and Methods, Cohesion and Coupling between Objects, Coupling and Visibility, Interfaces, Interfaces with Ball and Socket Notation, Templates, Analysis model vs. design model classes, Categorizing classes: entity, boundary and control, Modeling associations and collections


Unit IV

System Implementation and Project Management

A. Packages and interfaces: Distinguishing between classes/interfaces, Exposing class and package interfaces, Component and deployment diagrams: Describing dependencies, Deploying components across threads, processes and processors


Unit V

Principles of Testing


B. Analysis of Flow Graphs, Complexity Measures and computations
Text Books

Reference Books

Additional Reading
CS49102:: DATABASE MANAGEMENT SYSTEMS

Credits: 02  
Teaching Scheme: - Theory 2 Hrs/Week

Prerequisites: Nil

Objectives:
- To understand use of MS Excel and Macro programming as a data management application
- To understand importance of Database Management System (DBMS) over traditional file processing system
- To understand design of entity relationship and relational model
- To use SQL to create database objects, populate tables, and retrieve data
- Study relational databases such as MS Access and Oracle
- Mapping with PEOs :- II, III, (d)

Unit I  (5+1 Hrs)
MS Excel for Data Handling and Macro Programming

A. Introduction to MS Excel: Handling a small database using MS Excel, Data Import and Export facility; Functions and Formulas: Formulas with Several Operators and Cell Ranges, Using AutoCalculate; Data Analysis and PivotTables: Creating a PivotTable, Creating Subtotals; What-If Analysis, Macro Programming, Visual Basic Code, Prompting for User Input, Using If Then-Else Statement.
B. Generate Reports and Charts, Mathematical, Database Functions.

Unit II (6+1 Hrs)
Introduction to DBMS and E-R Data Model

A. Data Storage: File Processing System, Disadvantages; DBMS: Definition, Need of DBMS, System Architecture of DBMS; ER Model: Entity, Entity Set, Attributes, Primary Key, Relationship, Types and Attributes of Relationship, Role, Cardinality Ratio, Participation Constraint.
B. Data Abstraction, EER features.

Unit III (5+1 Hrs)
Introduction to Relational Model

A. Relational Data Model: Relation, Schema, Attributes, Tuples, Primary Key and Foreign Key, Relational Model Constraints, ER to Relational Mapping.
B. Participation Constraints, Life Cycle of a Relational Database.
Unit IV  
(6+1 Hrs)

Structured Query Language (SQL)

A. SQL: Introduction, SELECT Queries: Simple and Nested Queries, Set Membership, Aggregate Functions, Group by, Having Constructs, Join queries; DML: Insert, Delete and Update Queries, TCL; DDL: Create, Drop various Database Objects (Table, Table Constraints); Using SQL in MS Access, Oracle.

B. Set Operations in SELECT, Alter various Database Objects (Table, Table Constraints).

Unit V  
(5+1 Hrs)

Introduction to MS Access

A. MS Access: Introduction to MS Access, Database Implementation, Defining keys, Schema Diagram, Query Builder, Designing Forms for data manipulation

B. Generating Reports in MS Access.

Text Books


Reference Books

CS49302:: DATABASE MANAGEMENT SYSTEMS

Credits: 01  
Teaching Scheme: - Laboratory 2 Hrs/Week

Prerequisites: Nil

Objectives:
- Use of MS Excel
- Database design using ER and Relational model
- Implementation of database system in MS Access, Oracle
- Use of SQL for data manipulation
- Mapping with PEOs :- II, III, (d)

List of Practical

1. Use MS Excel for Data handling, apply Formula and Functions.
2. Generate Reports and Charts in MS Excel.
3. Implement Macro in MS Excel.
4. Draw an Entity-Relationship diagram for your proposed database.
5. Translate above E/R model to Relational model.
6. Create Database in MS Access.
7. Use Query Builder of MS Access.
9. Use SQL for Data manipulation and data retrieval in MS Access, Oracle.

Text Books
Reference Books