



Vishwakarma Institute of Technology, Pune

Academic Time Table

2010-2011

First Term

ETC - TE - G

(Branch-Year-Division)

Hour	8:15	9:15	10:30	11:30	1:30	2:30	3:45	4:45	TEACHING STAFF
MON	-	-	1224 VPP1 MECXTh	1224 SBB2 DCTh	1-1223-RVT1-DSP / 1 1207 MMS1 MECX		1224 PAK1 LICATh	1224 RVT1 DSPTh	BGT1-B G Tarlekar
	-	-	-	-	-	-	-	-	SVK1-S V Kurundkar
	-	-	-	-	3-1208-VDG1-EDTut	-	-	-	VPP1-V P Parkhi
	-	4-1208-VDG1-EDTut	-	-	-	-	-	-	VSJ1-V S Jabade
	-	-	-	-	-	-	-	-	SML1-S M Lambor
TUE	-	-	1224 RVT1 DSPTh	1224 PAK1 LICATh	1-1220A-VSJ1-LICA		1224 SBB2 DCTh	1224 BGT1 MCATh	ABB1-A B Barbadekar
	-	-	-	-	-	-	-	-	SSB1-S S Bhatlawande
	-	-	-	-	-	-	-	-	VDG1-V D Gaikwad
	-	-	-	-	4-1207-MMS1-MECX		-	-	MMS1-M M Shidore
	-	-	-	-	-	-	-	-	PAK1-P A Kulkarni
WED	-	-	1224 SBB2 DCTh	1224 RVT1 DSPTh	-	-	-	-	VRM2-V R More
	-	-	-	-	2-1223-RVT1-DSP		2-1220A-VSJ1-LICA		SBB2-S B Bhise
	-	-	-	-	3-1207-MMS1-MECX		-	-	RVT1-R V Tornekar
	-	-	-	-	-	-	-	-	SUBJECT
	-	-	-	-	-	-	-	-	Microcontroller Applications
THU	-	-	1224 BGT1 MCATh	1224 BGT1 MCATh	1-1221-VRM2-DC		-	-	Linear Integrated Circuits and
	-	-	-	-	-	-	-	-	Digital Communication
	-	-	-	-	-	-	-	-	Digital Signal Processing
	-	-	-	-	-	-	4-1221-SBB2-DC		Electronics Design
	-	-	-	-	-	-	-	-	Mechatronics
FRI	-	-	1224 PAK1 LICATh	1224 VPP1 MECXTh	-	-	1-1213D-ABB1-MCA		
	-	-	-	-	-	-	2-1221-VRM2-DC		
	-	-	-	-	3-1213D-SSB1-MCA		3-1220A-PAK1-LICA		
	-	-	-	-	-	-	-	-	
	-	-	-	-	-	-	-	-	
SAT	-	1-1208-VDG1-EDTut	-	-	-	-	-	-	
	2-1213D-ABB1-MCA		2-1208-VDG1-EDTut	-	-	-	-	-	
	3-1221-SVK1-DC		-	-	-	-	-	-	
	4-1220A-SML1-LICA		4-1213D-ABB1-MCA		-	-	-	-	
	-	-	-	-	-	-	-	-	
							W.E.F. 12/07/2010		Authorized Signatory
Sequence:- Batch no (if Lab/Tut), Room no, Staff, Subject							TH:14 LAB:16 TUT:4		Developed by MCA III (07-08),VIT

