



Vishwakarma Institute of Technology, Pune

Academic Time Table

2010-2011

First Term

ELX - TE - F

(Branch-Year-Division)

Hour	8:15	9:15	10:30	11:30	1:30	2:30	3:45	4:45	TEACHING STAFF
MON	-	-	1202 KSD1 DCTh	1202 VSJ1 LICATh	1-1207-MMS1-MECX / 1 1223 RVT1 DSP		1202 DGK1 MCATh	1202 SSB1 DSPTh	KSD1-K S Deogaonkar
	-	-	-	-	-	-	-	-	BGT1-B G Tarlekar
	-	-	-	-	-	-	-	-	SVK1-S V Kurundkar
	-	-	-	-	-	-	-	-	VSJ1-V S Jabade
	-	-	-	-	-	-	-	-	SSB1-S S Bhatlawande
TUE	-	-	1202 SSB1 DSPTh	1202 KSD1 DCTh	1-1208-VDG1-EDTut	-	1201 DGK1 MCATh	1201 VSJ1 LICATh	VDG1-V D Gaikwad
	-	-	-	-	-	2-1208-VDG1-EDTut	-	-	MMS1-M M Shidore
	-	-	-	-	-	-	-	-	PAK1-P A Kulkarni
	-	-	-	-	-	-	-	-	SBB2-S B Bhise
	-	-	-	-	-	-	-	-	RVT1-R V Tornekar
WED	-	-	1202 MMS1 MECXTh	1202 VSJ1 LICATh	1-1213D-SSB1-MCA		-	-	DGK1-Dnyanshwar Gajanan Kanade
	-	-	-	-	-	-	2-1221-SBB2-DC		
	-	-	-	-	3-1208-VDG1-EDTut	-	3-1213D-BGT1-MCA		
	-	-	-	-	-	-	-	-	SUBJECT
	-	-	-	-	-	-	-	-	Microcontroller Applications
THU	-	-	1202 DGK1 MCATh	1202 MMS1 MECXTh	-	-	-	-	Linear Integrated Circuits and
	-	-	-	-	2-1220A-PAK1-LICA		2-1213D-BGT1-MCA		Digital Communication
	-	-	-	-	3-1207-MMS1-MECX		3-1220A-VSJ1-LICA		Digital Signal Processing
	-	-	-	-	-	-	-	-	Electronics Design
	-	-	-	-	-	-	-	-	Mechatronics
FRI	-	-	1202 SSB1 DSPTh	1202 KSD1 DCTh	1-1221-SVK1-DC		-	-	
	-	-	-	-	2-1207-MMS1-MECX		-	-	
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	-	-	-	-	-	-	-	-	
SAT	-	-	1-1220A-PAK1-LICA		-	-	-	-	
	-	-	-	-	-	-	-	-	
	-	-	3-1221-SBB2-DC		-	-	-	-	
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							W.E.F.	12/07/2010	Authorized Signatory
	Sequence:- Batch no (if Lab/Tut), Room no, Staff, Subject						TH:14 LAB:12 TUT:3		Developed by MCA III (07-08),VIT

